

FIG. 1

09836781.061801

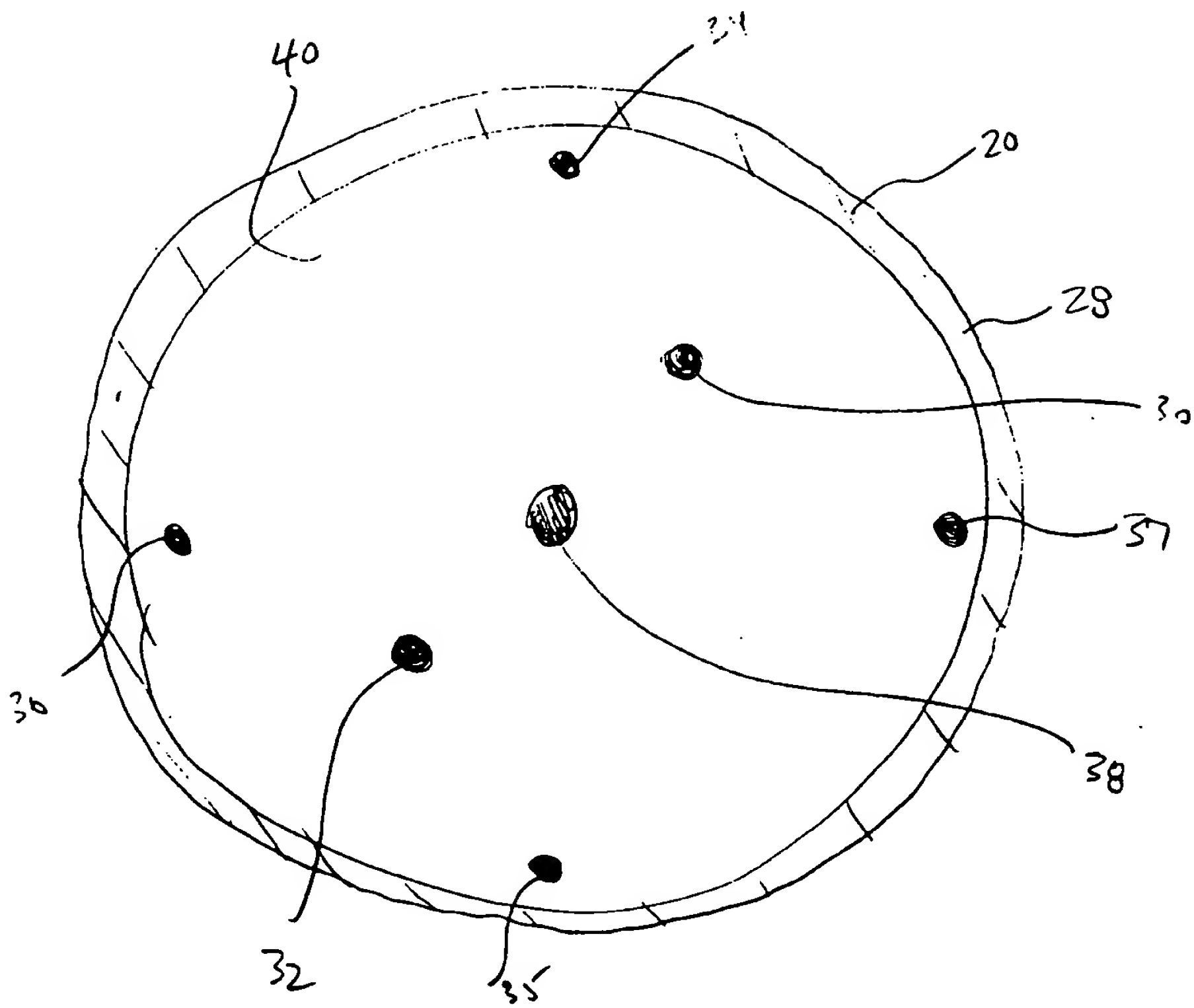


FIG. 3

09336781-061801

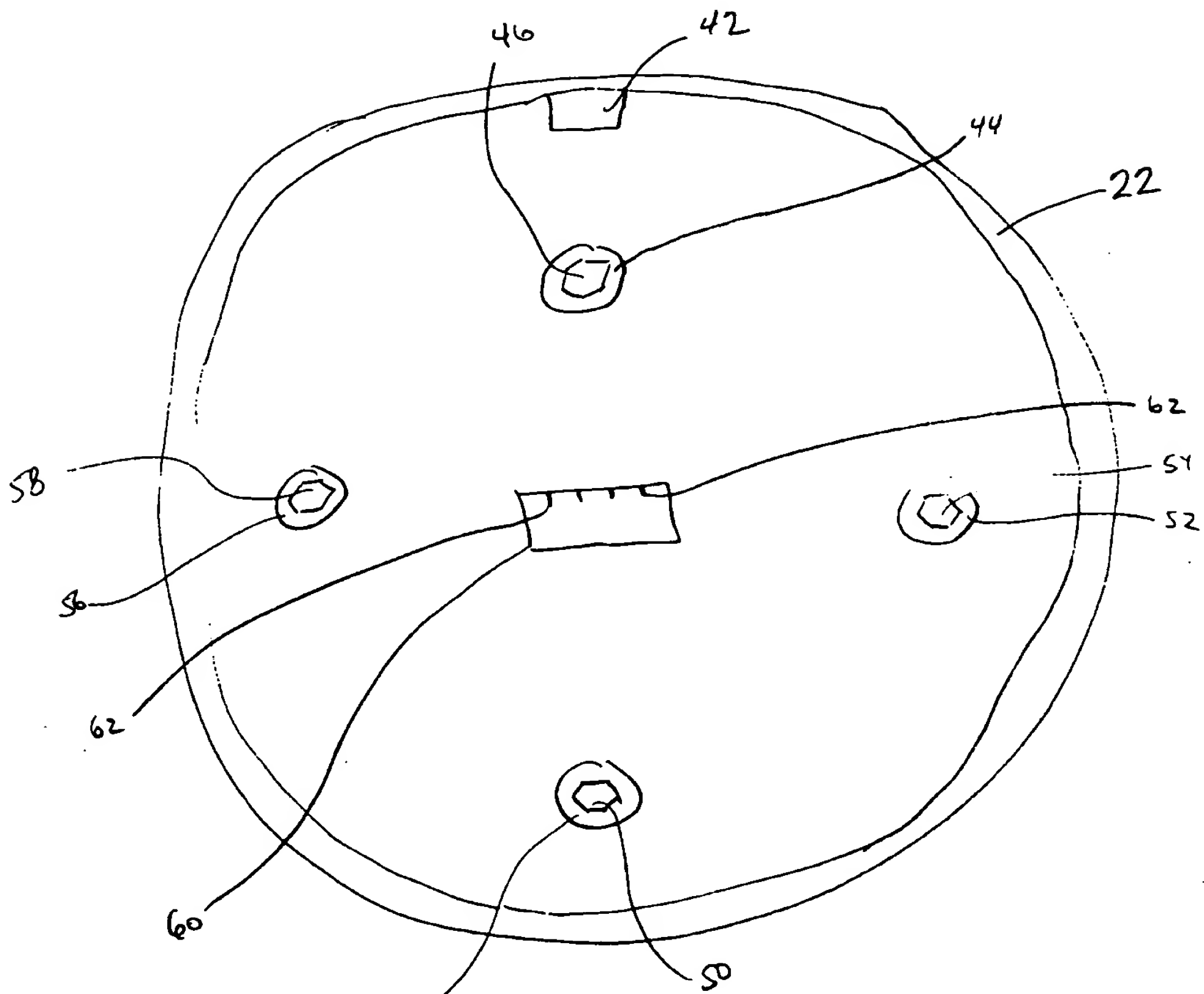


FIG. 4

09336781.061301

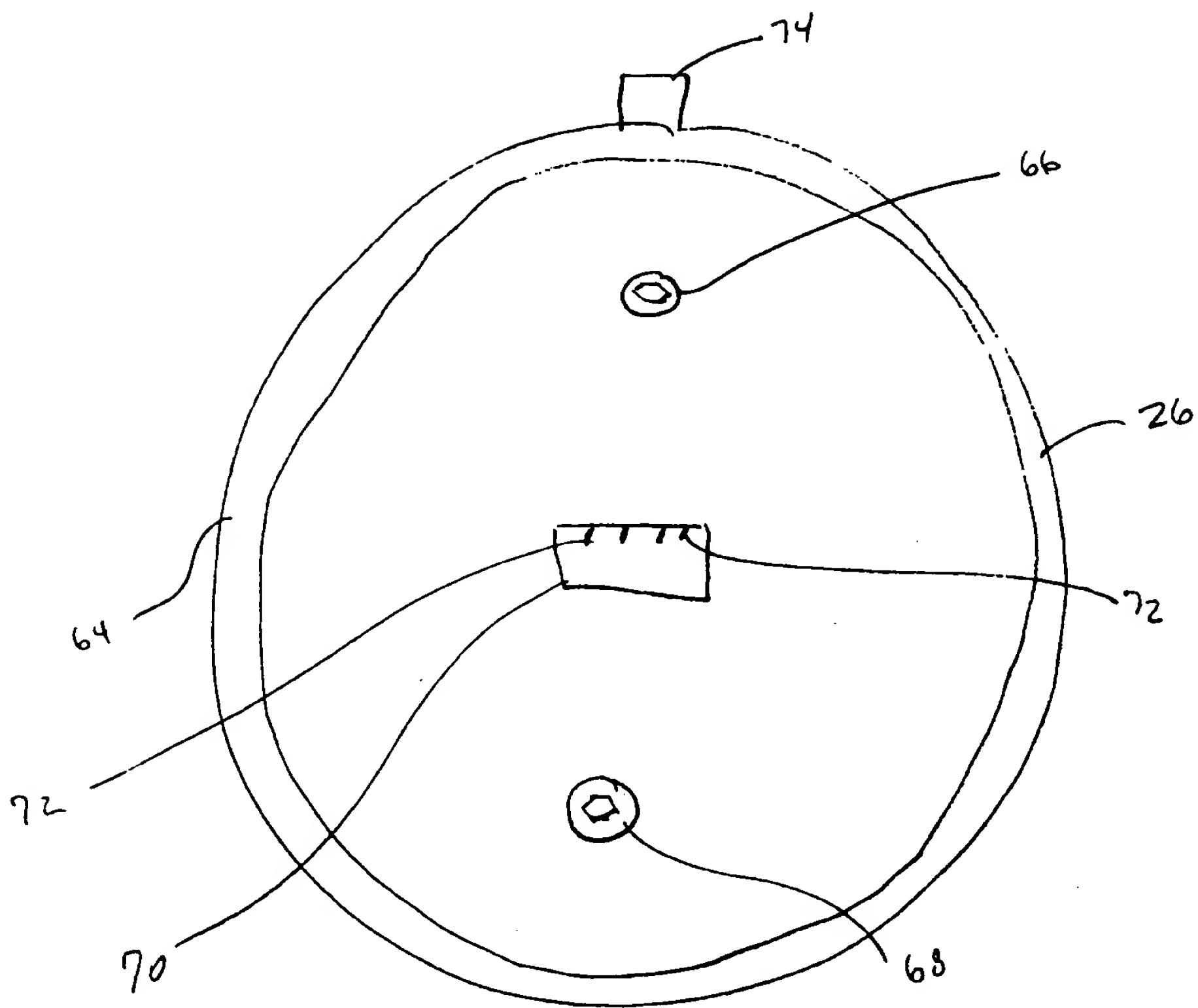


FIG. 5

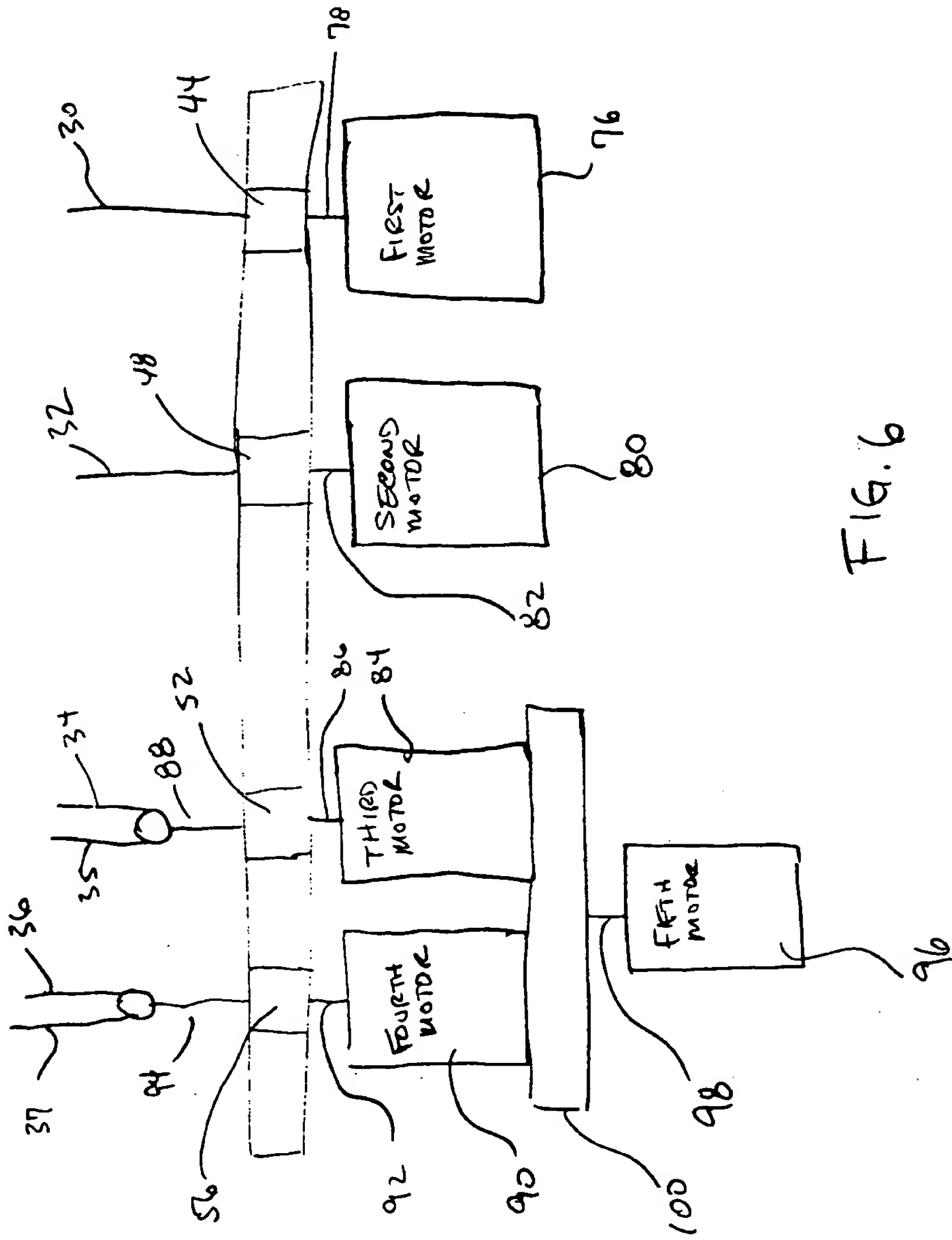


FIG. 6

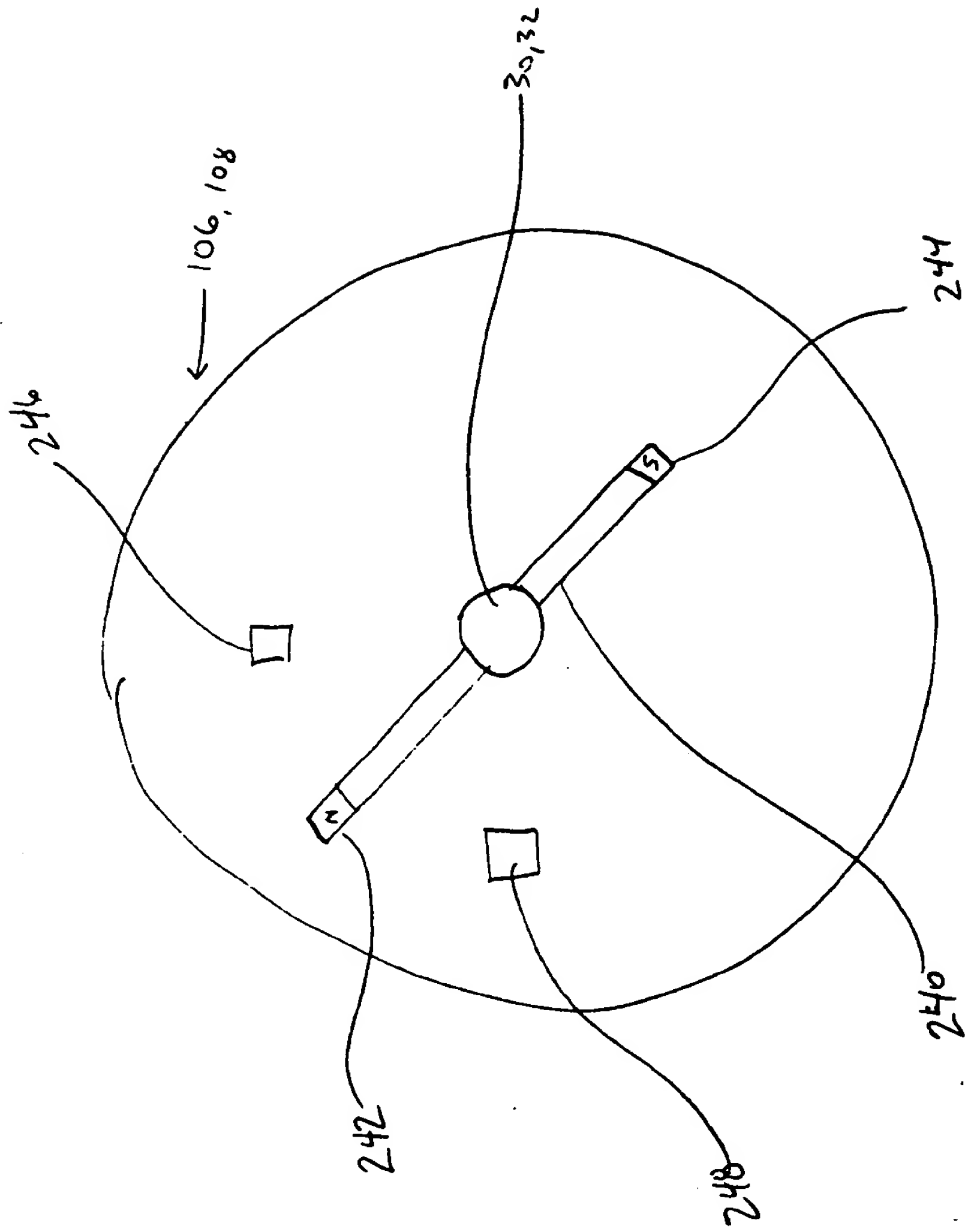


FIG. 8

09836781.061804

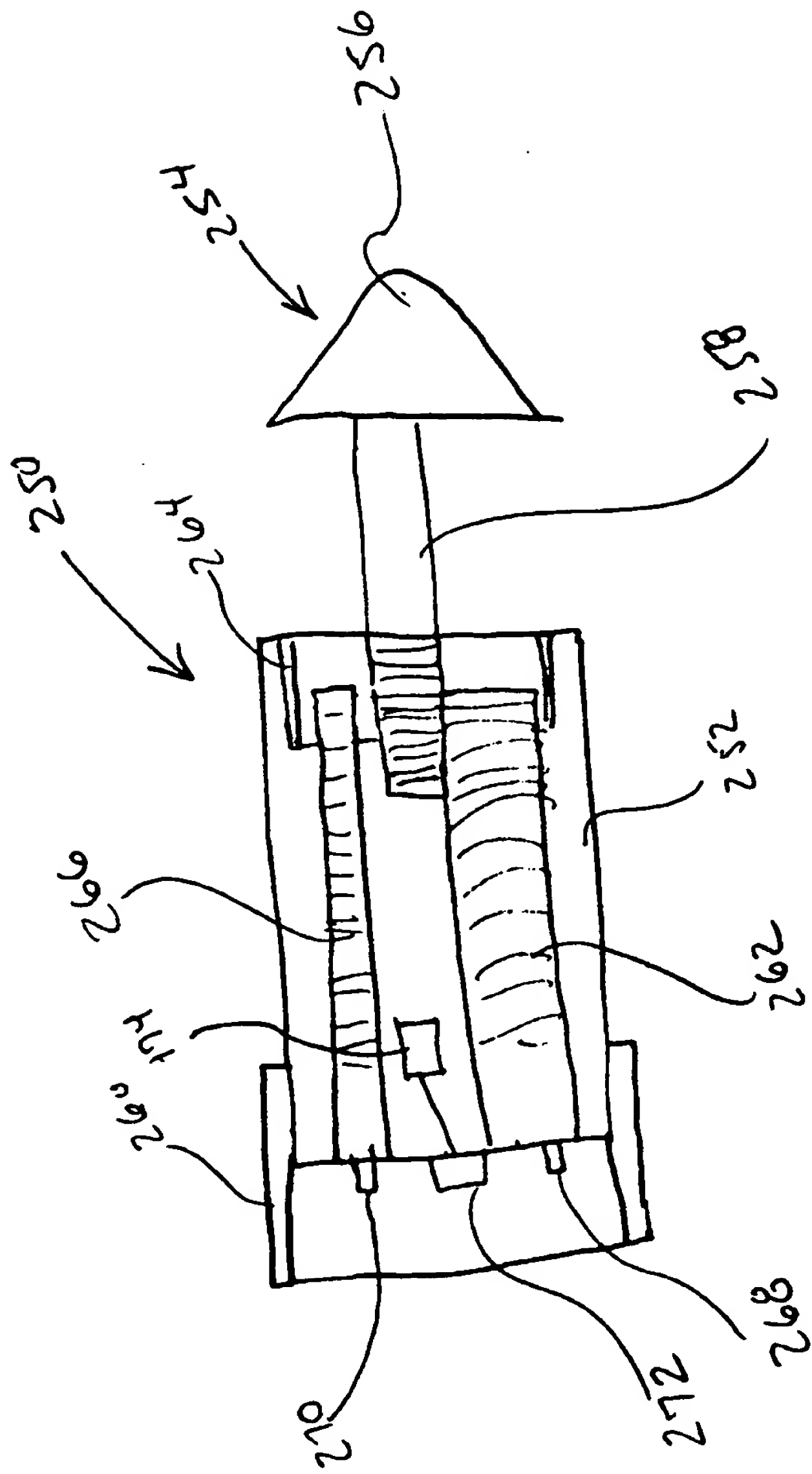


Fig. 9A

030616Z

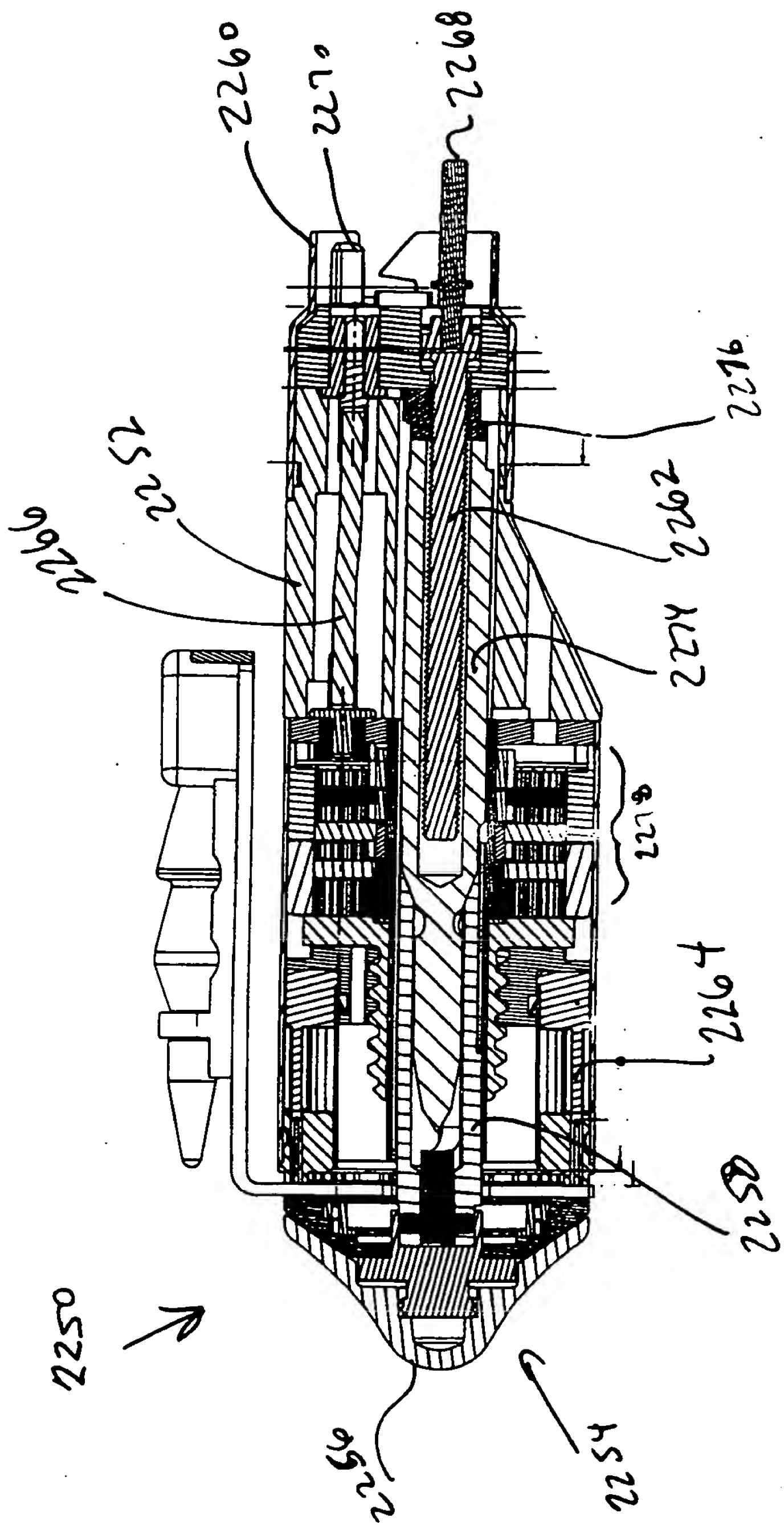


Figure 9B

09836781, 061804

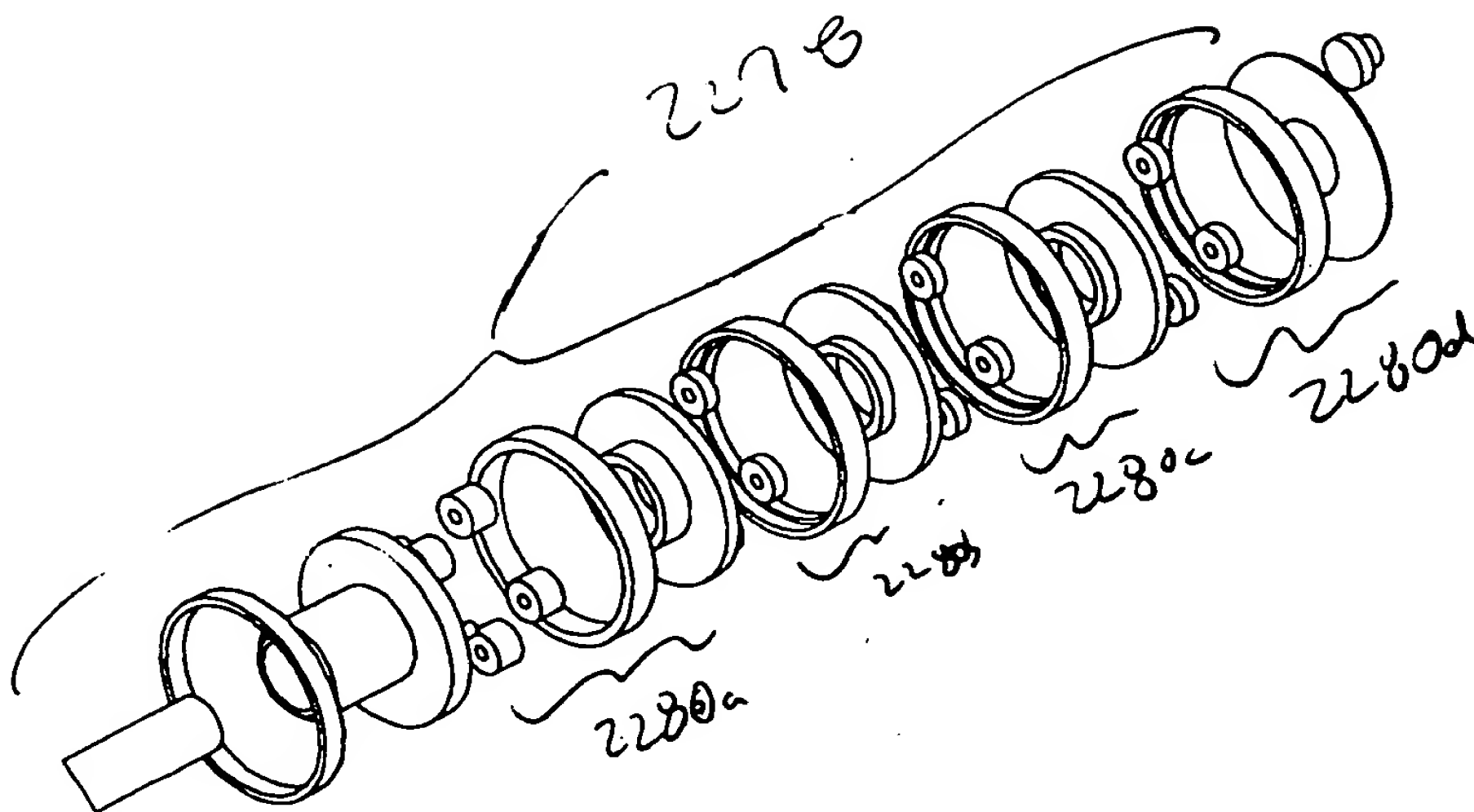


FIG. 9C

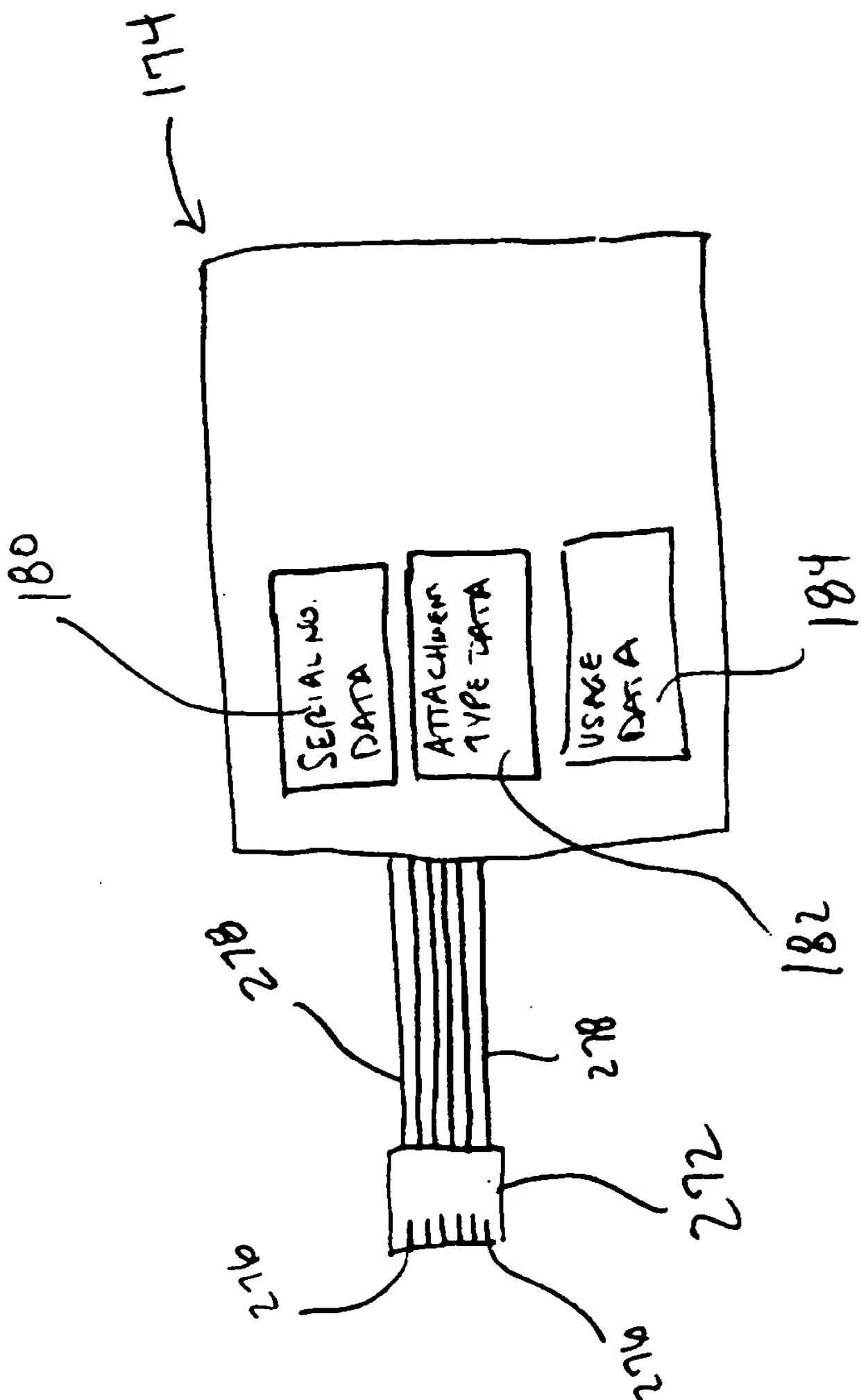
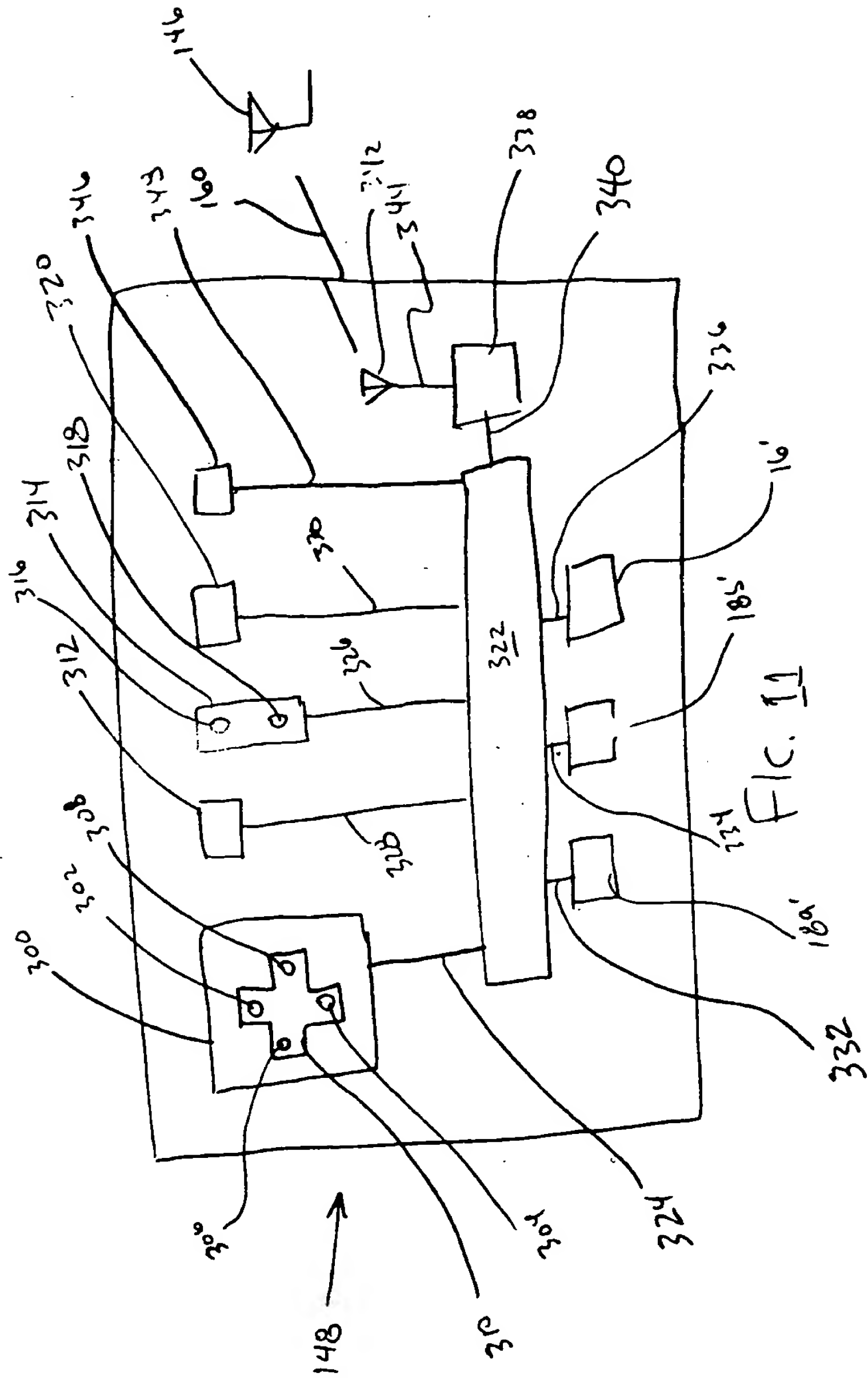


FIG. 10



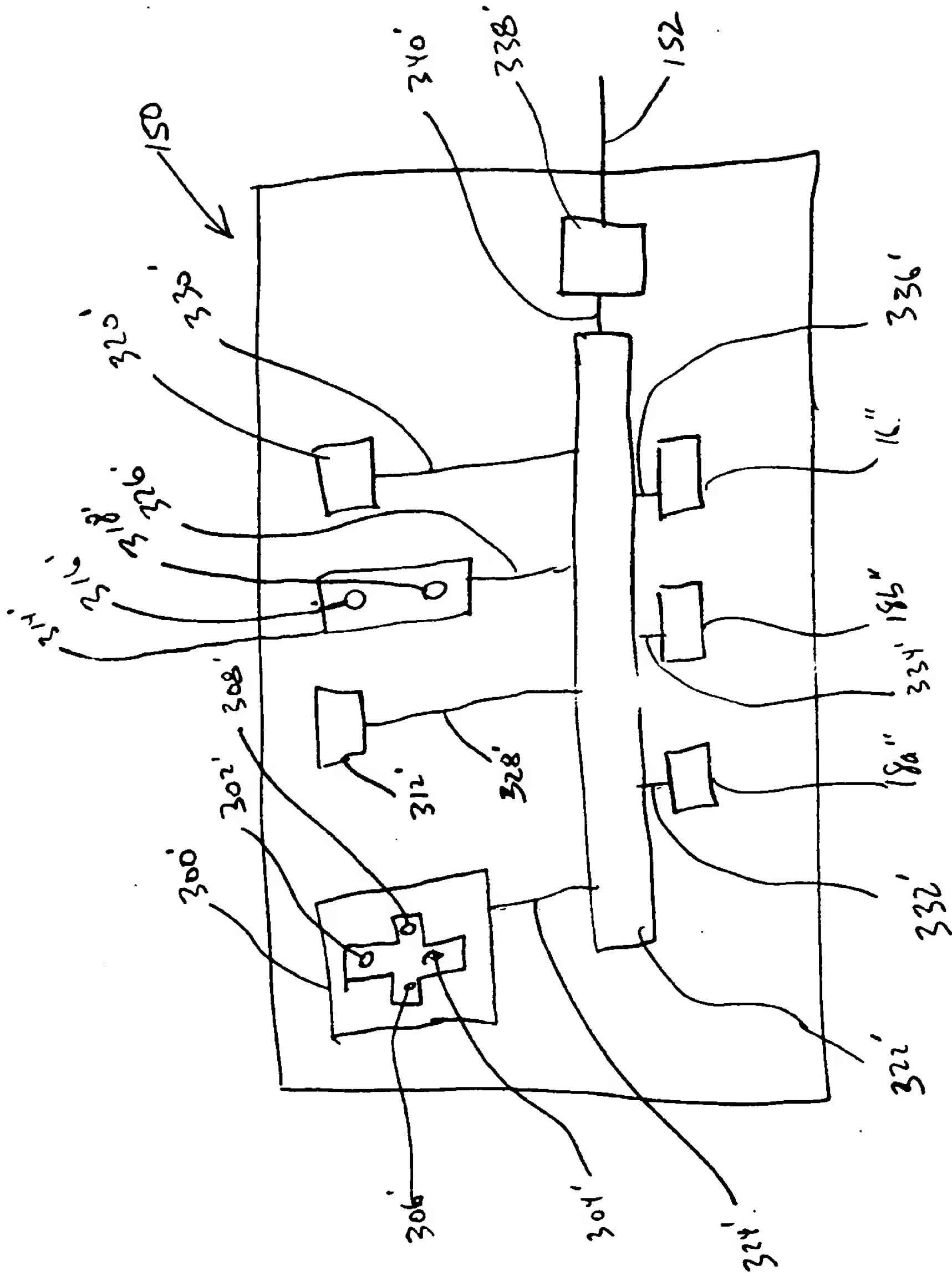


FIG. 12

03336706400
FOR RELEASE

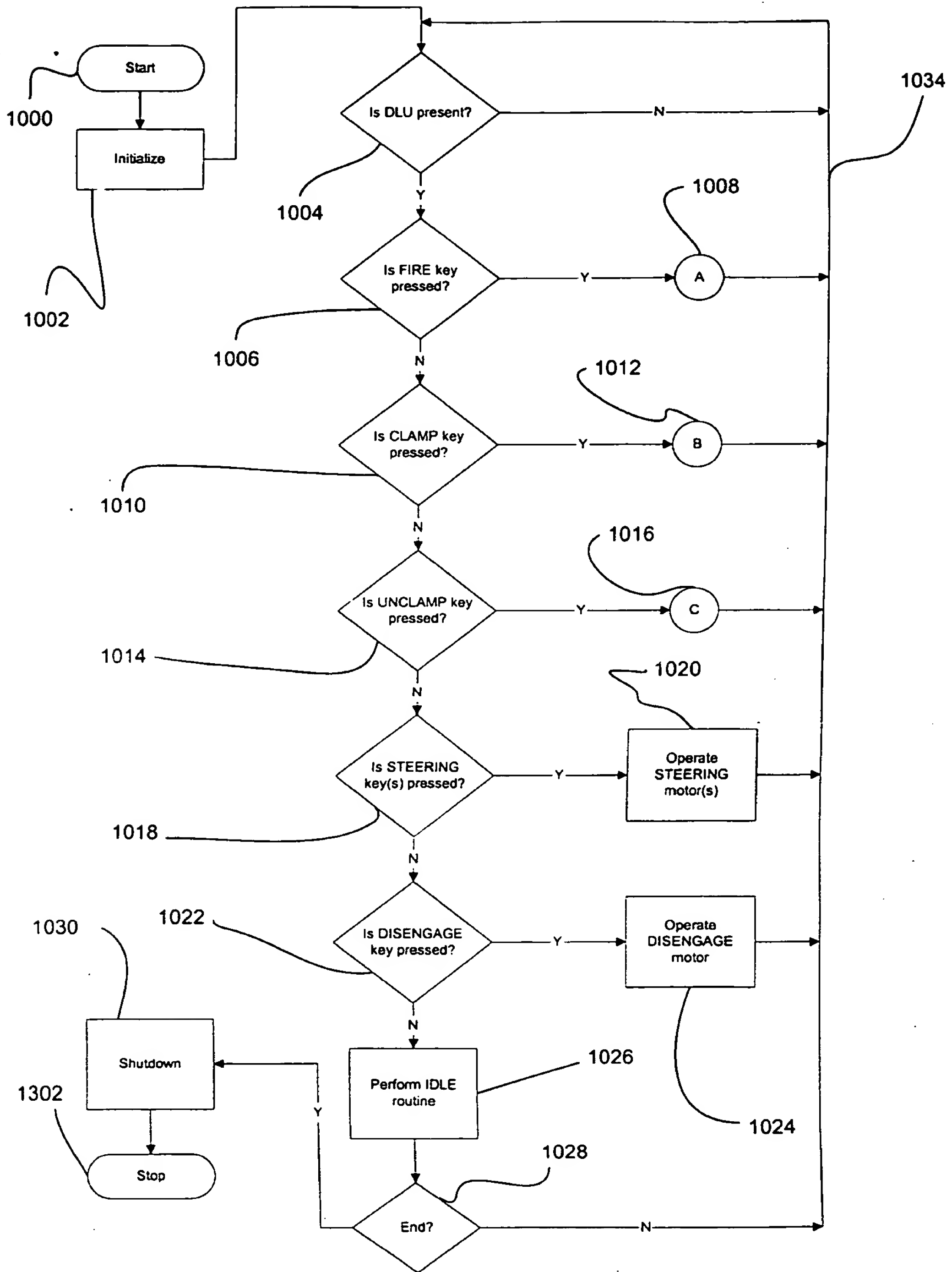


Figure 13

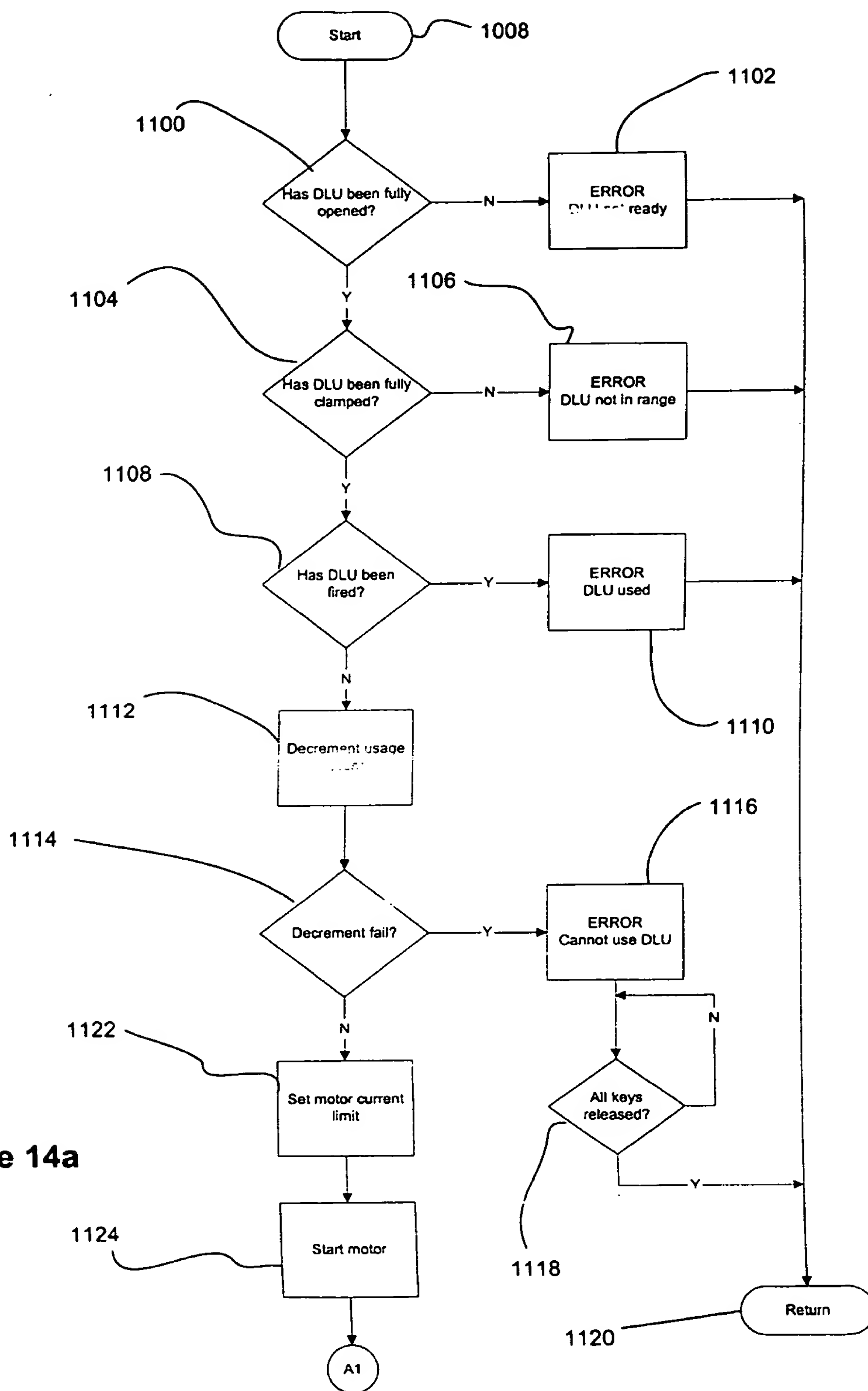
[illegible]

Figure 14a

SECRET

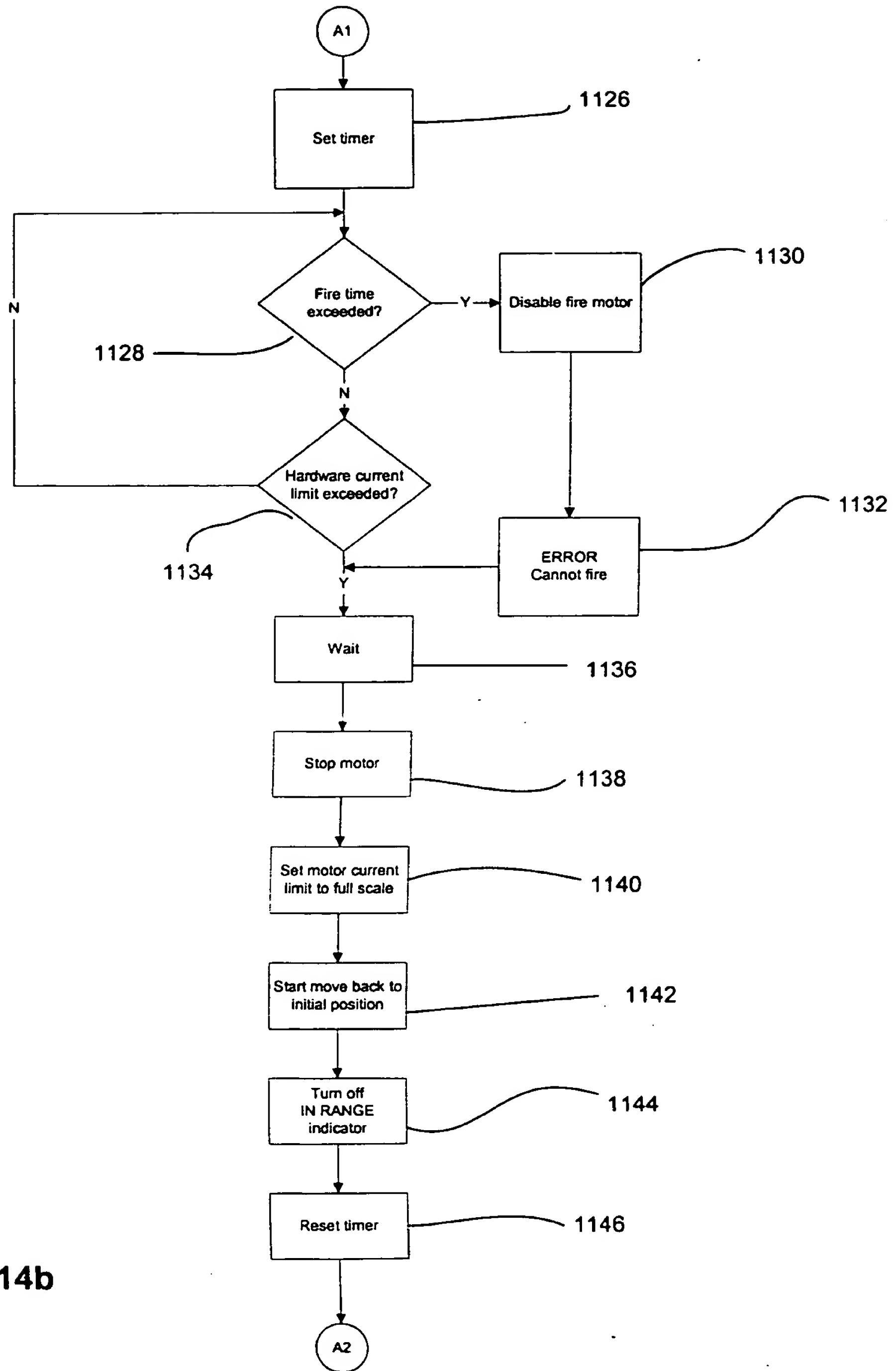
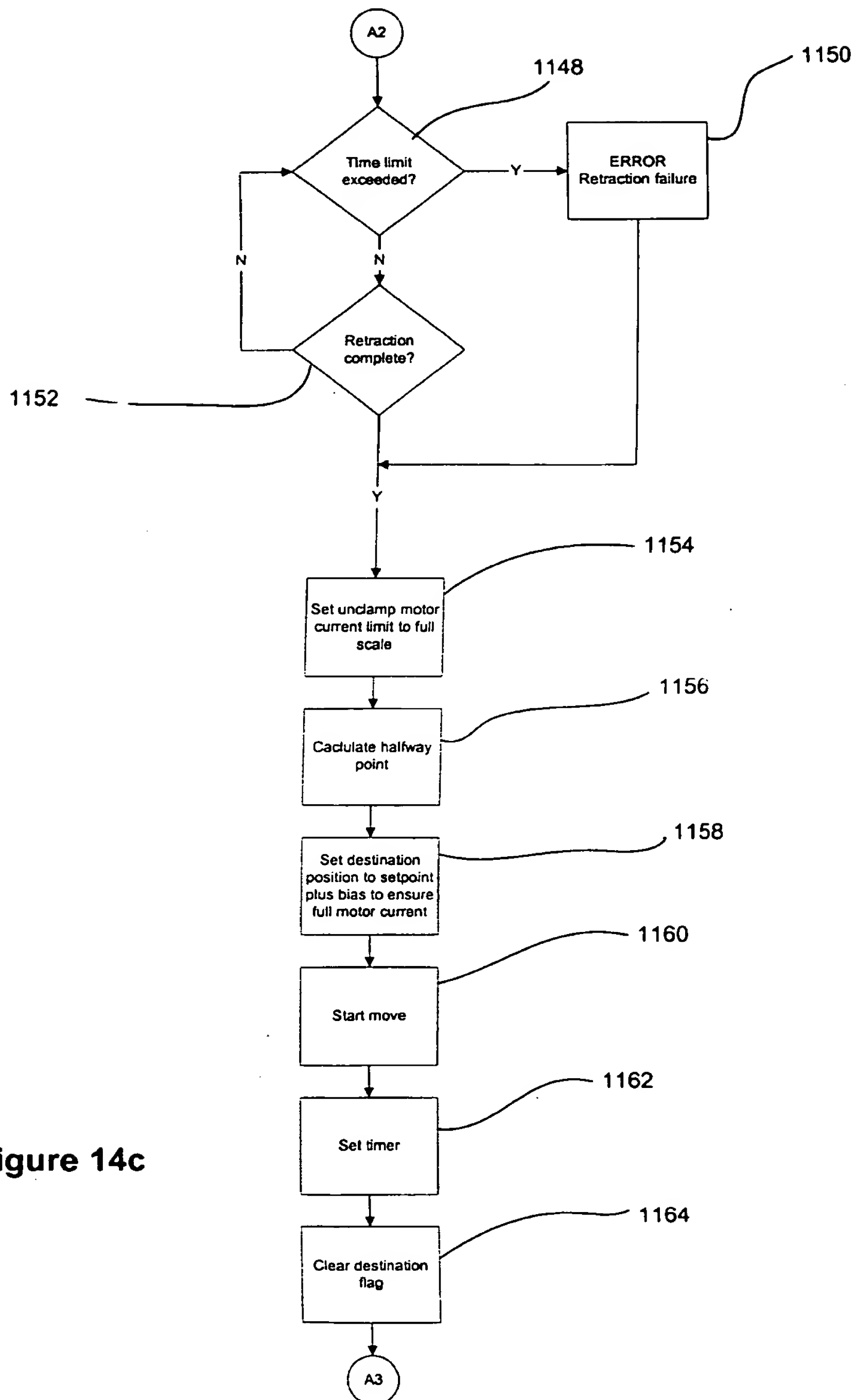


Figure 14b

Figure 14c



0936781-061001

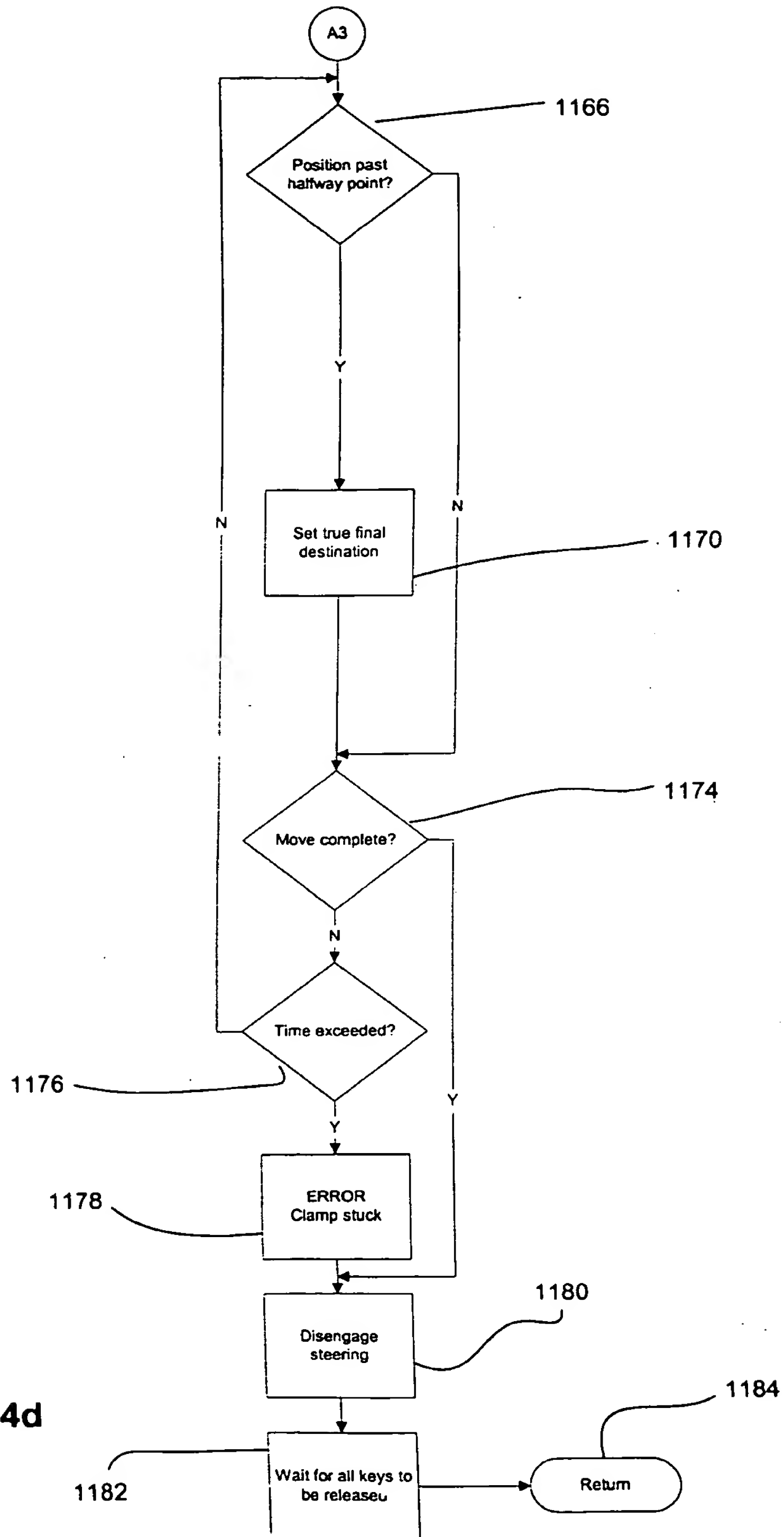


Figure 14d

09836784.061804

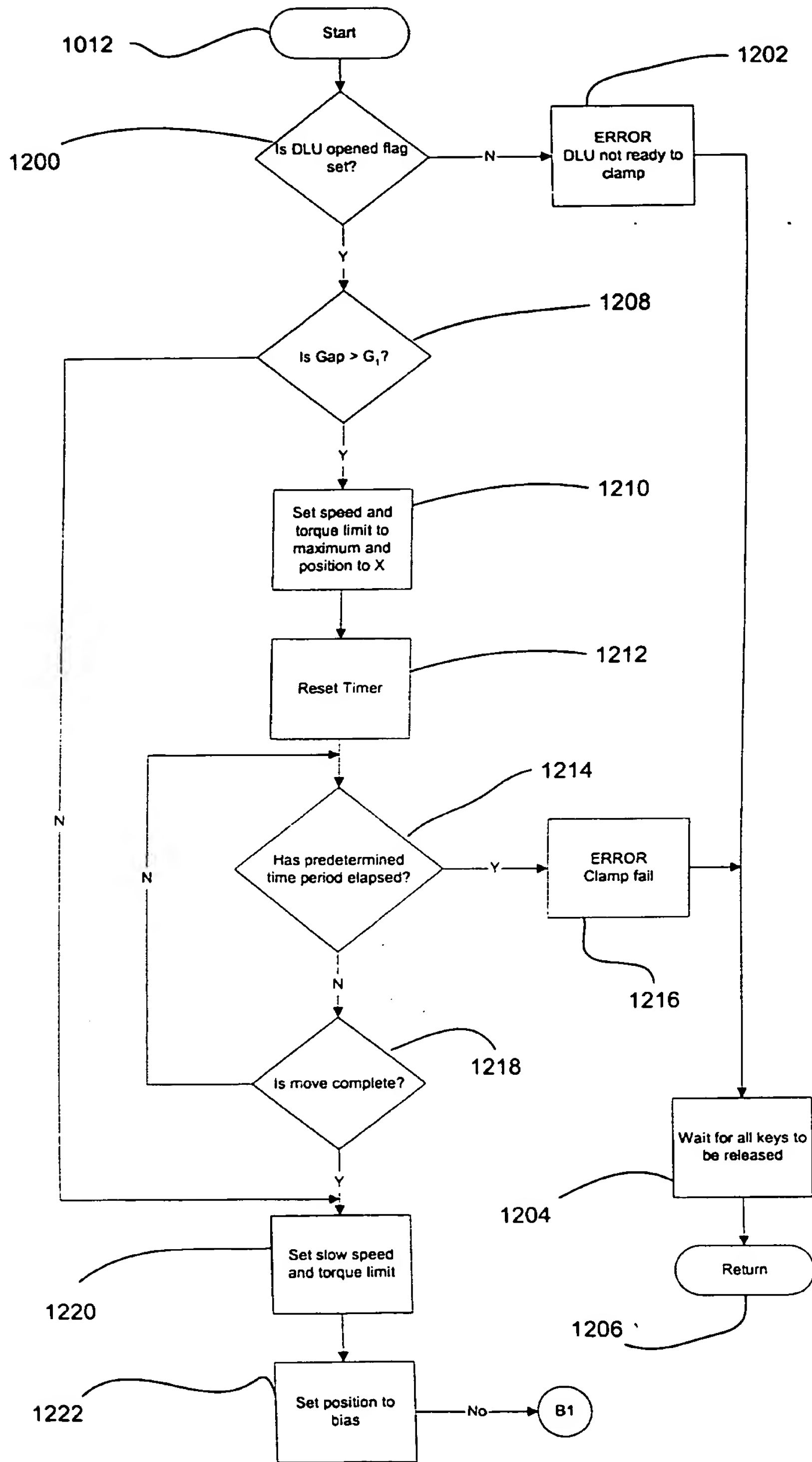


Figure 15a

09336791-061303

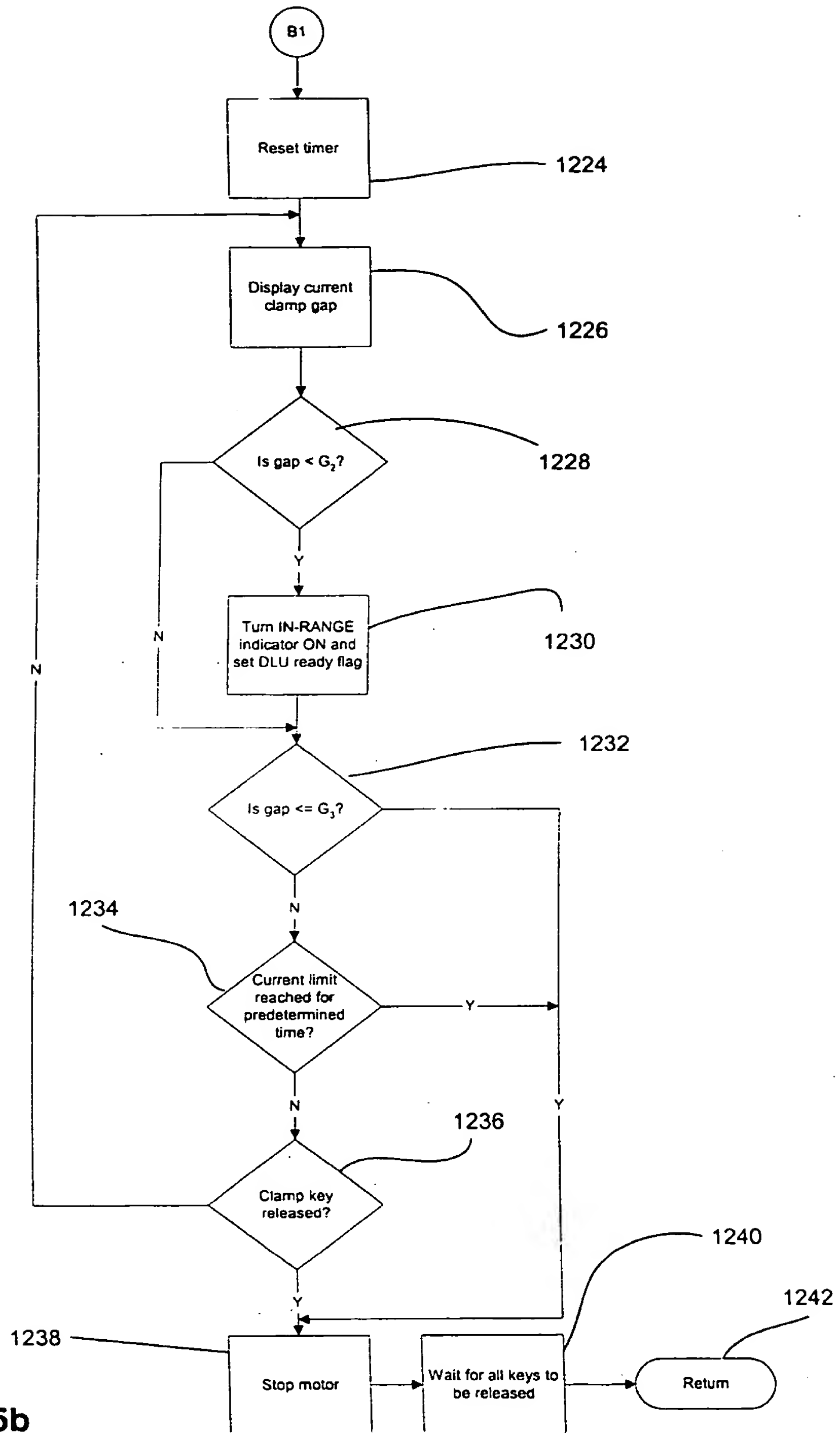


Figure 15b

THE UNIVERSITY OF CHICAGO

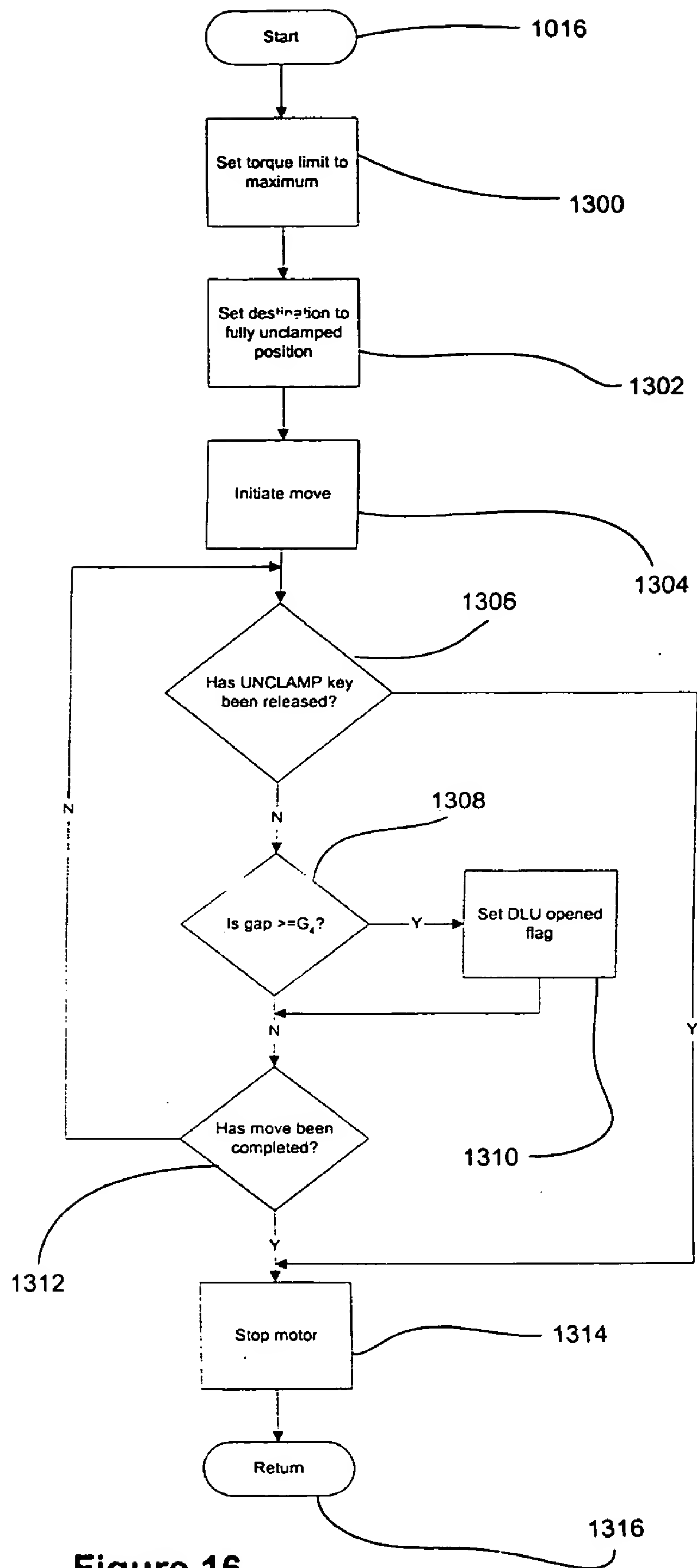


Figure 16

05636781.061801

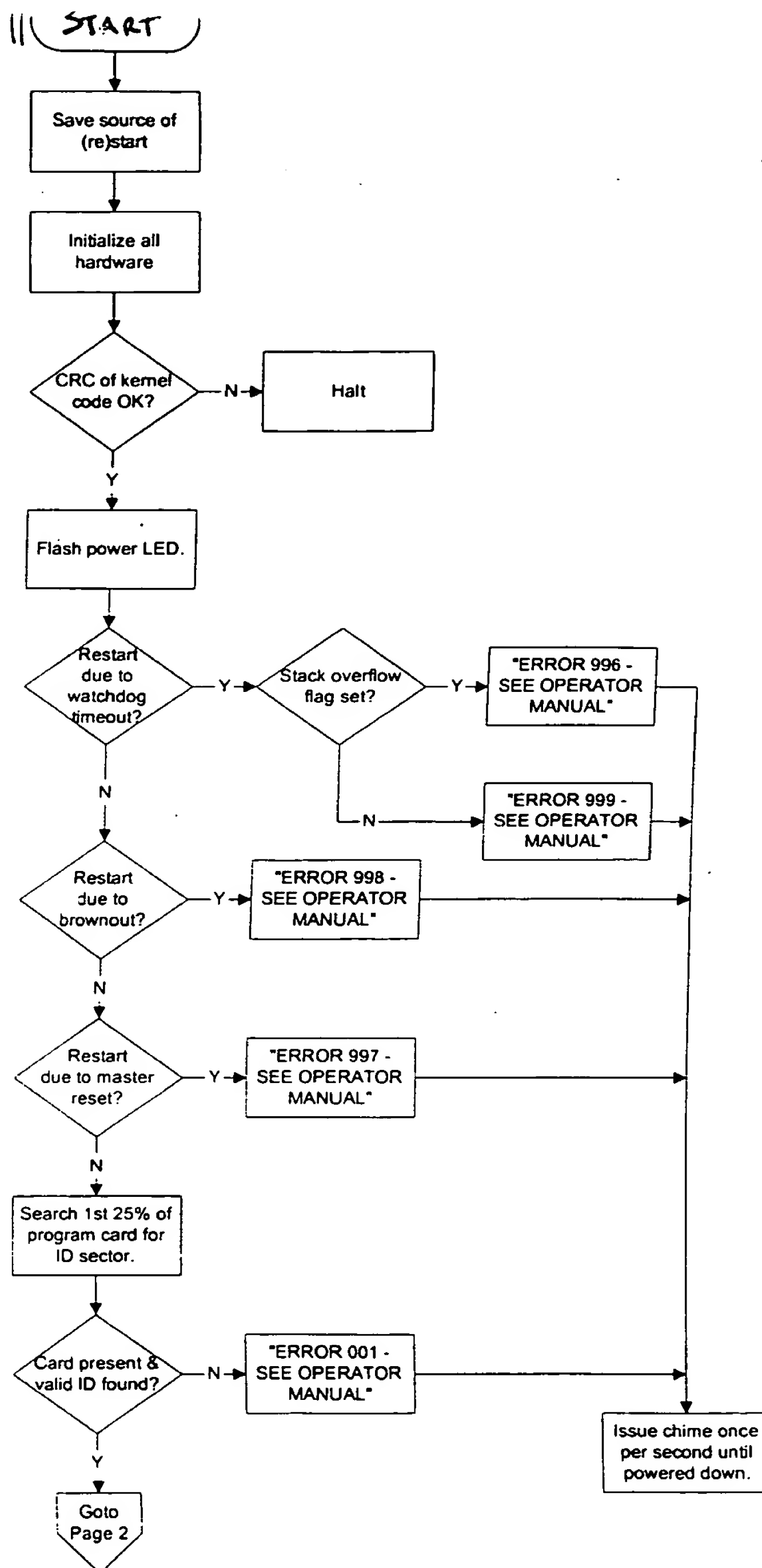


FIG. 17a

09030701-001001

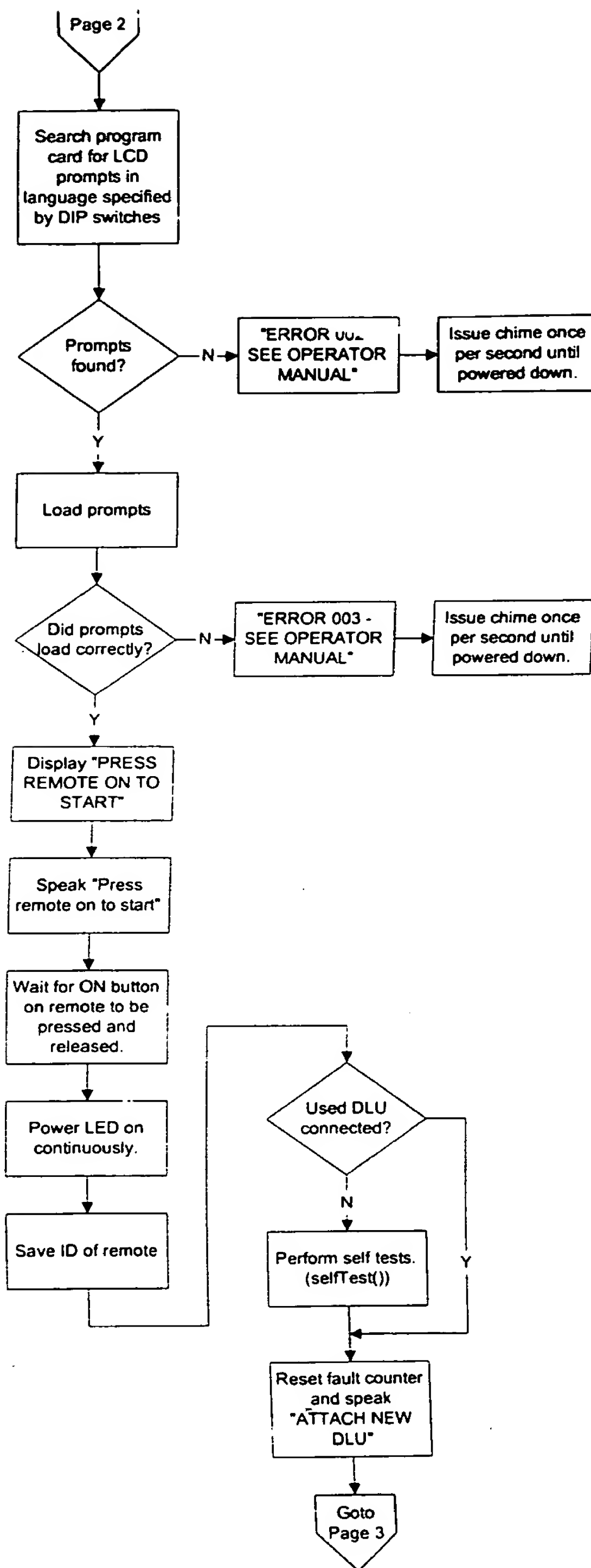


FIG. 17b

009036781 061801

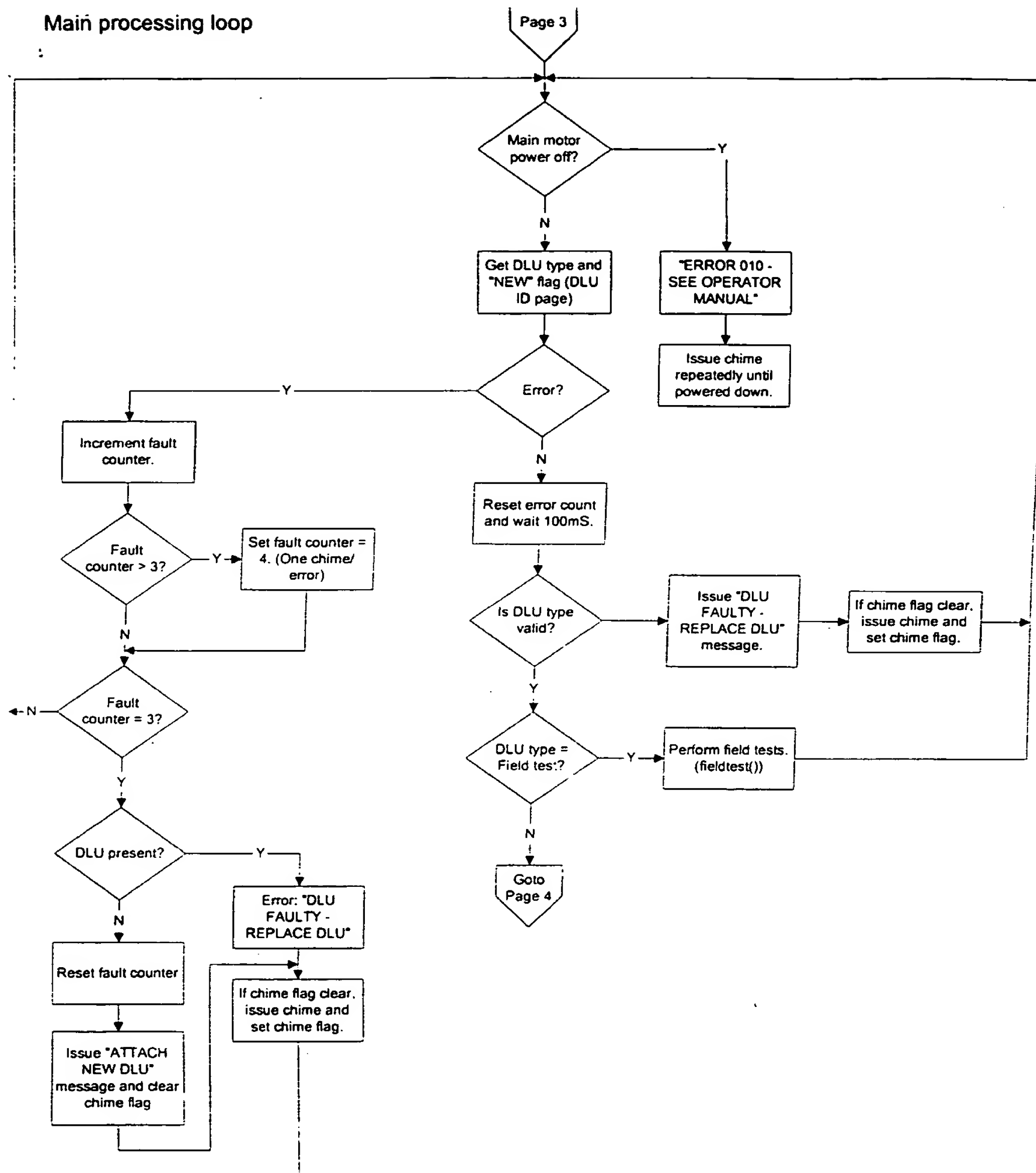


FIG. 17c

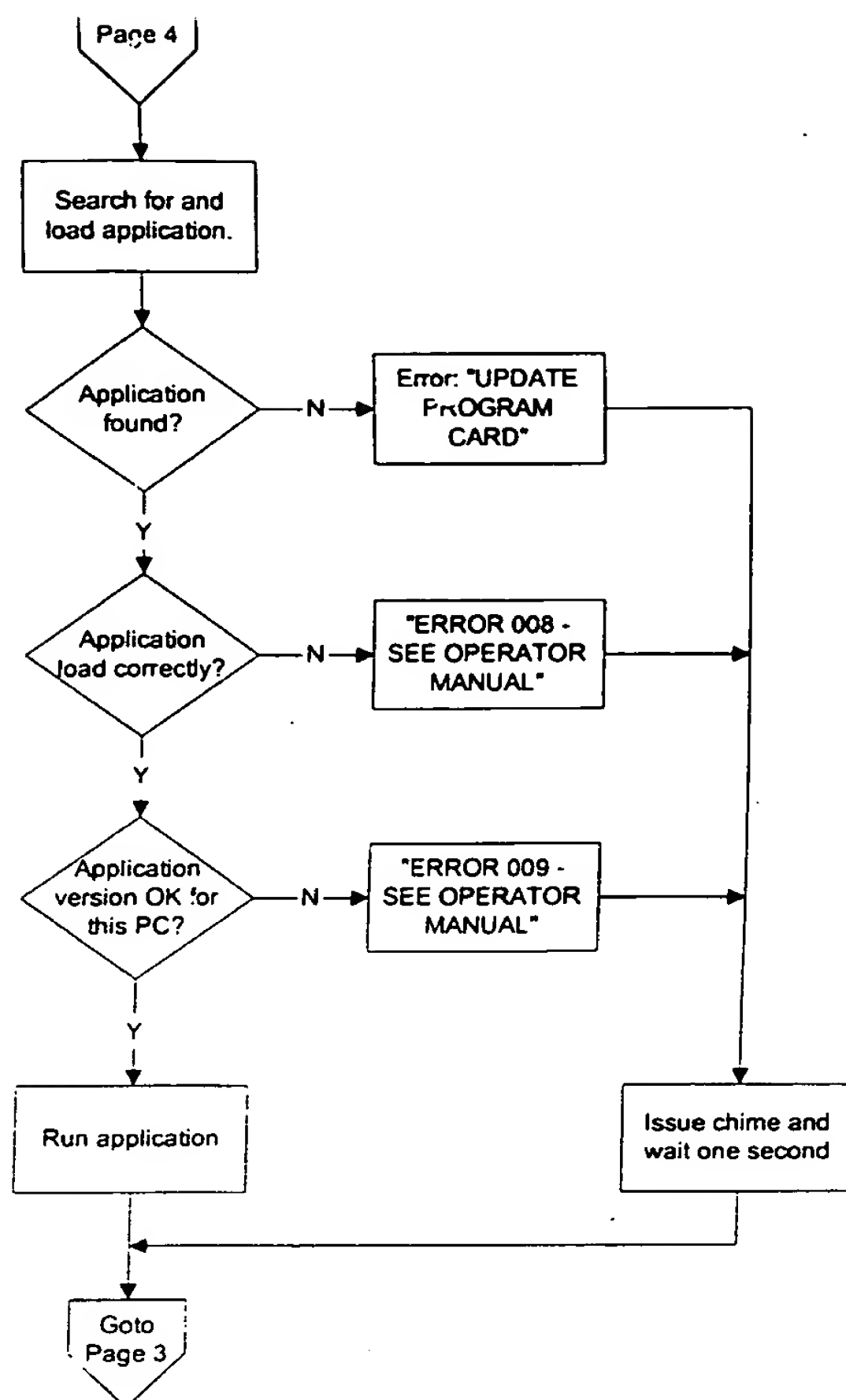
[illegible]

FIG. 17d

090316Z JUL 84 061300Z

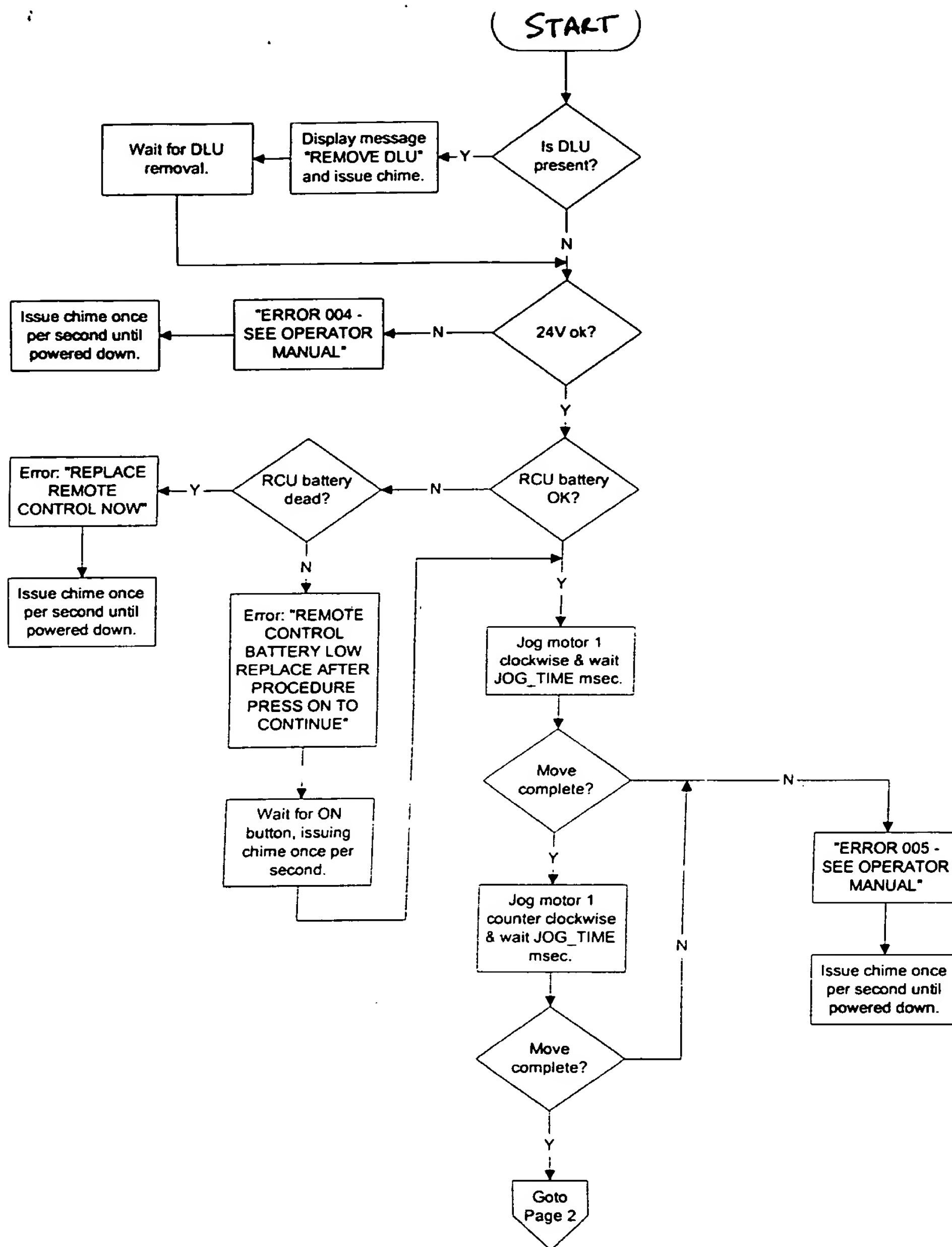


FIG. 18a

09036701 061801

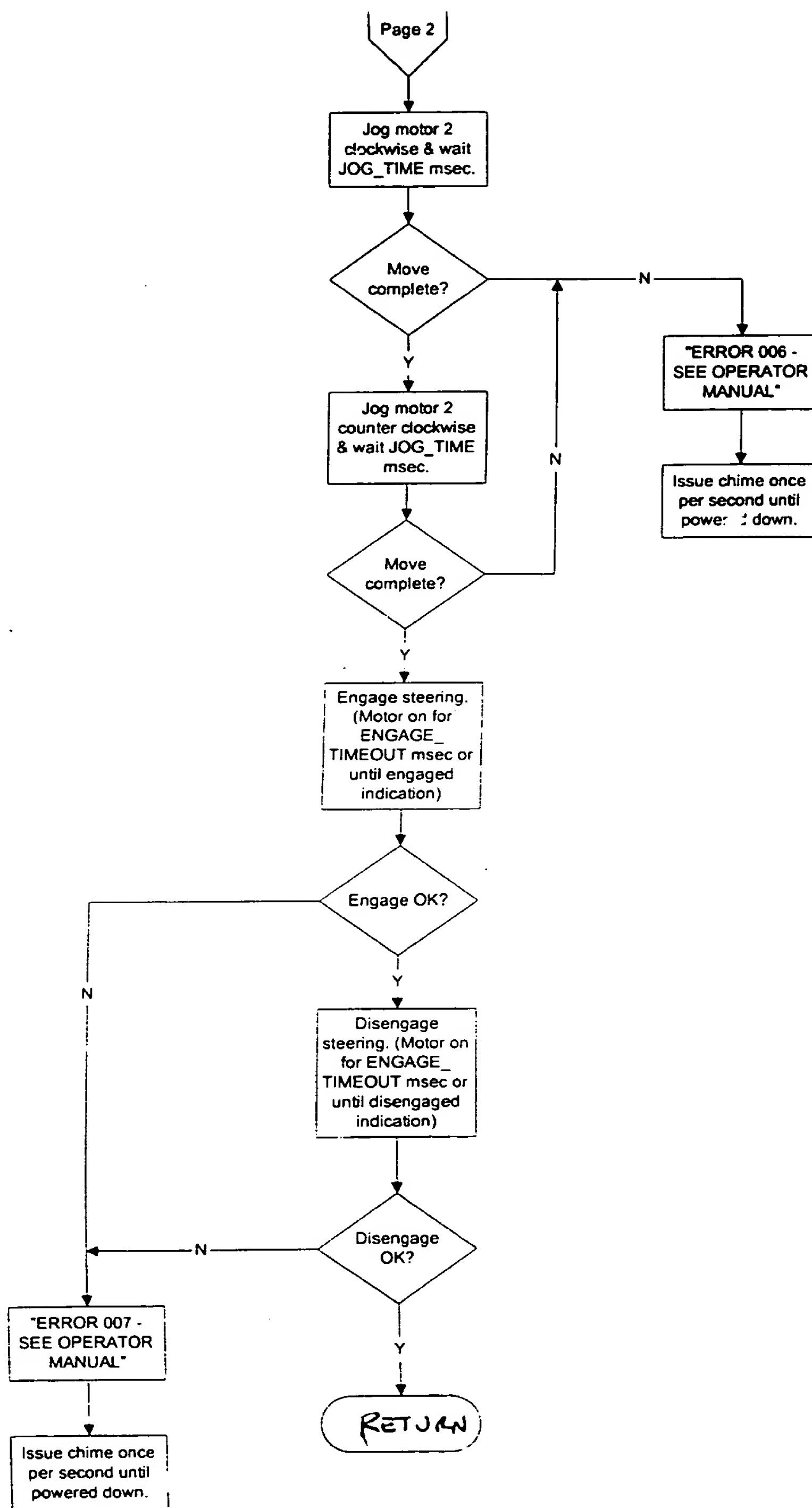


FIG. 186

09030701.001.001

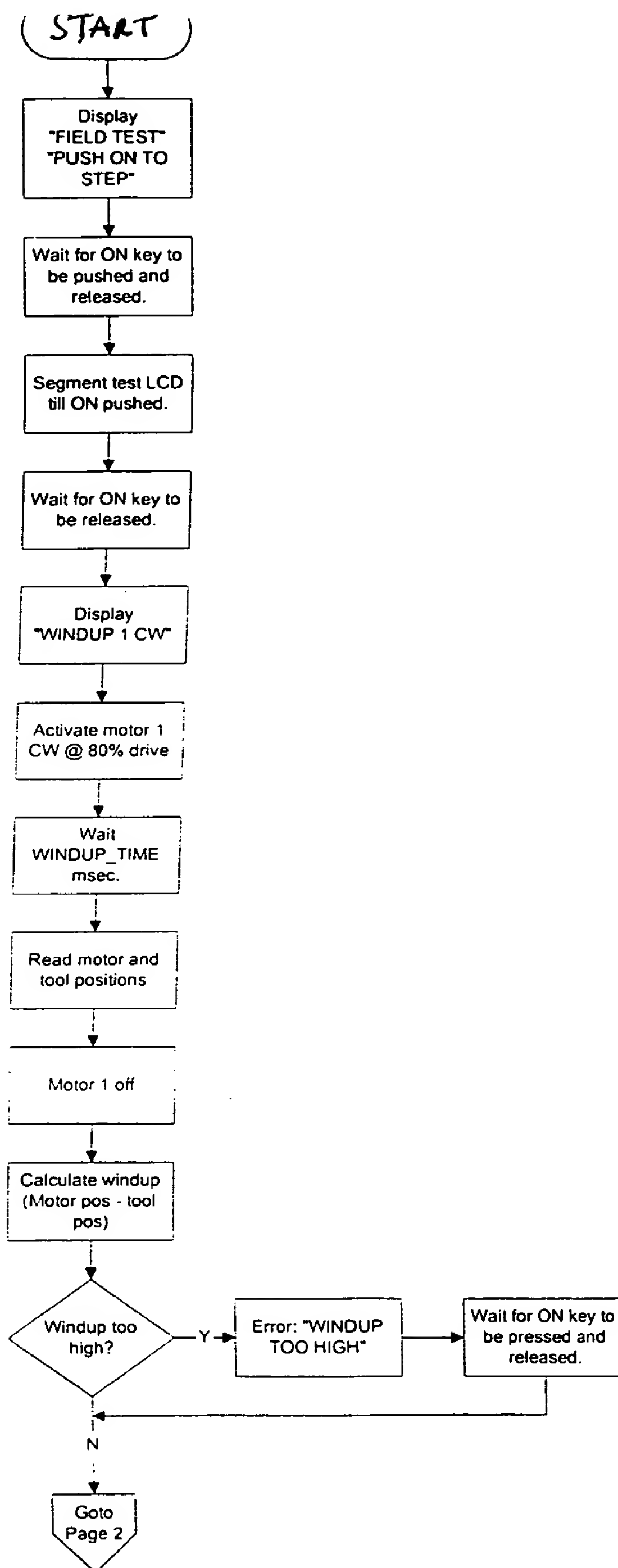


FIG. 19a

SECRET

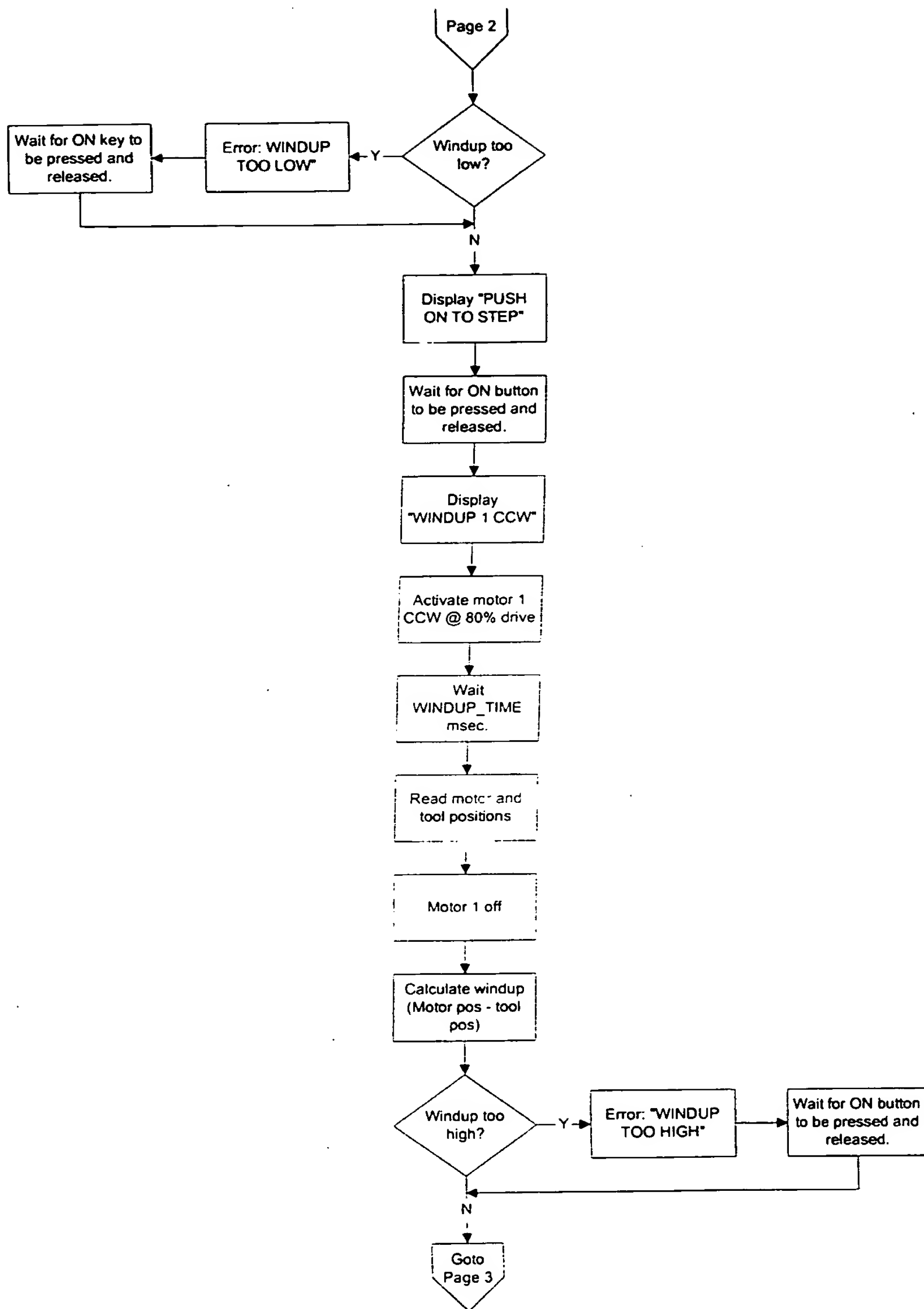


FIG. 196

09036781.001001

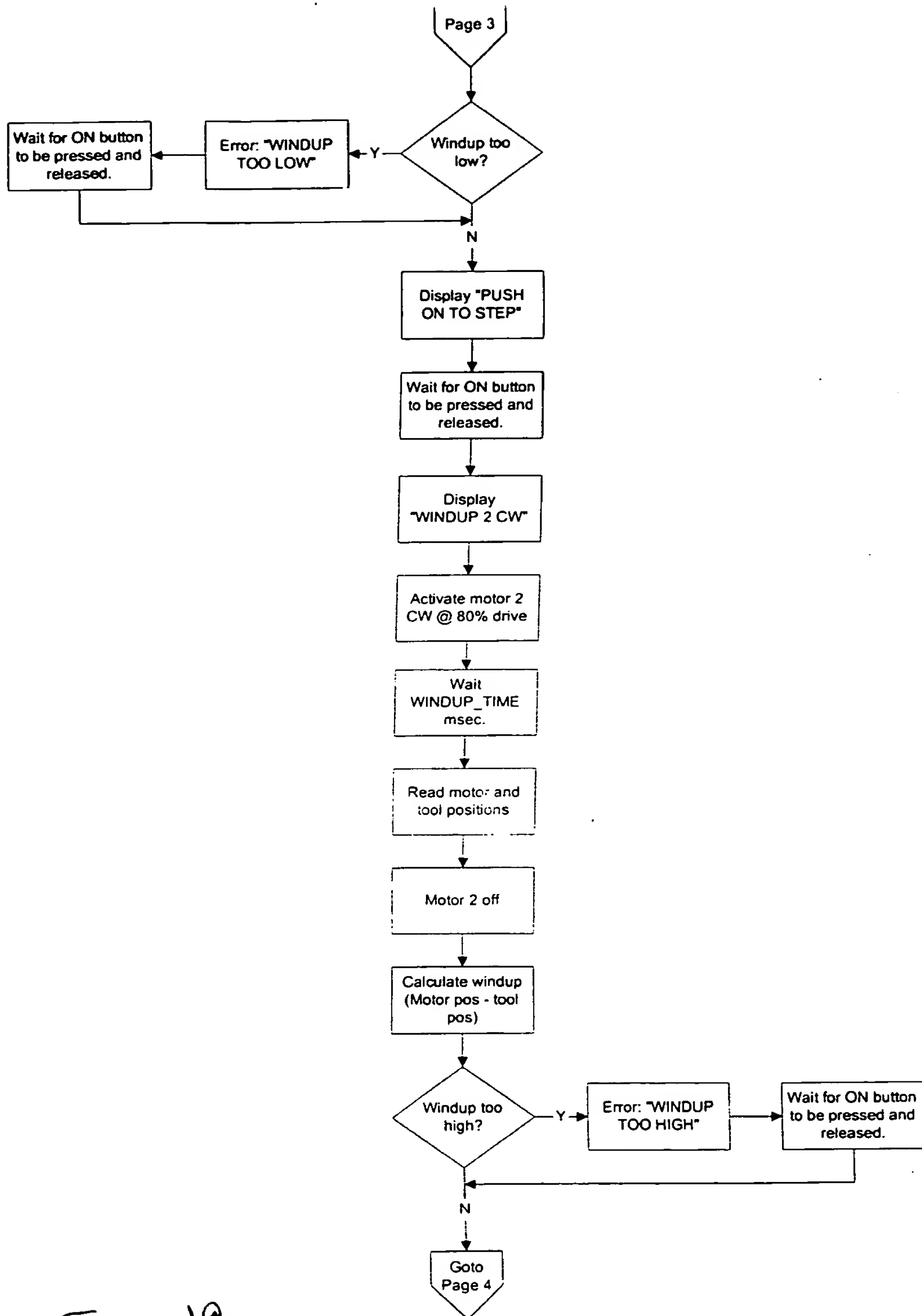
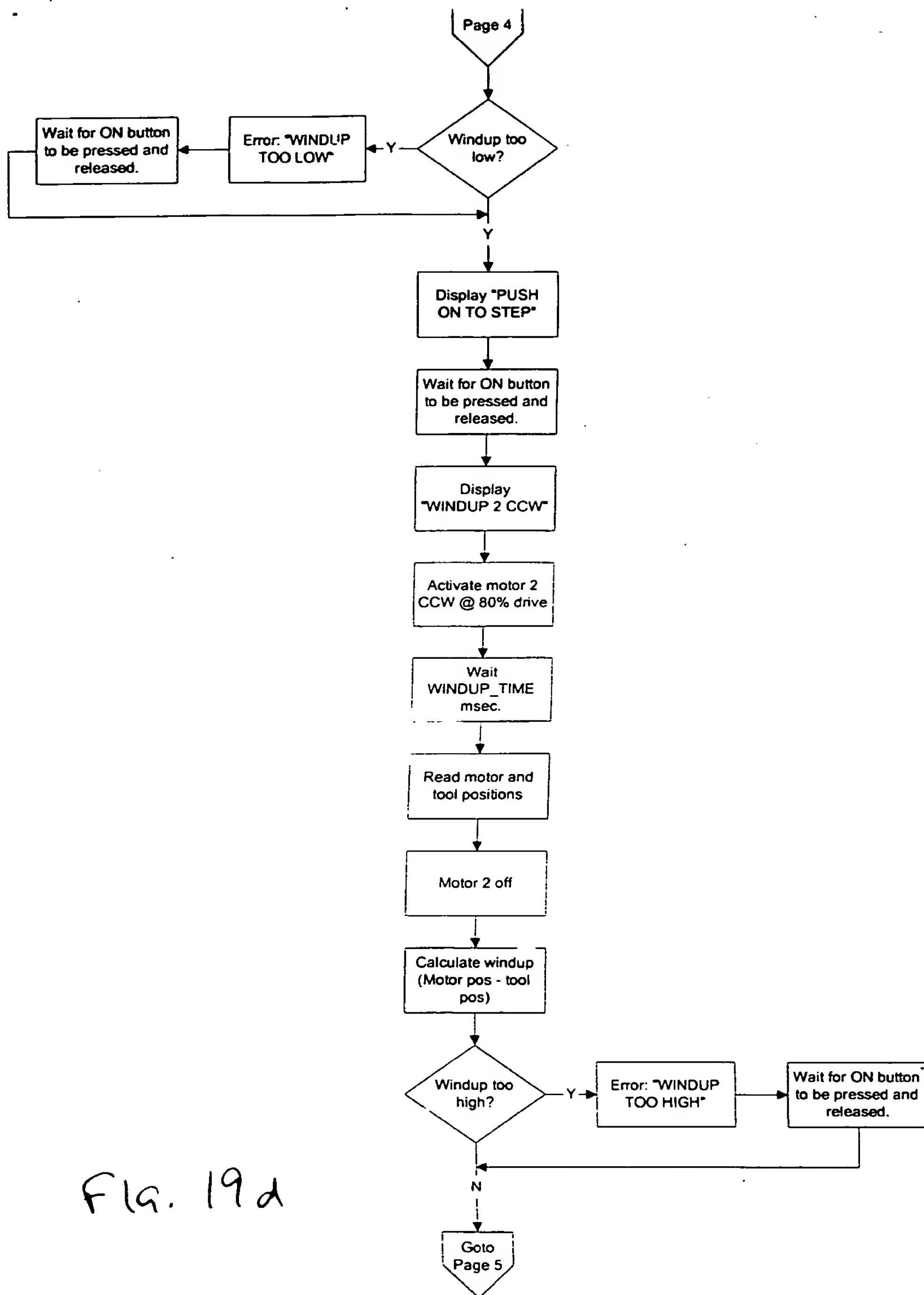


FIG. 19c

09036701-001001



09336781.061804

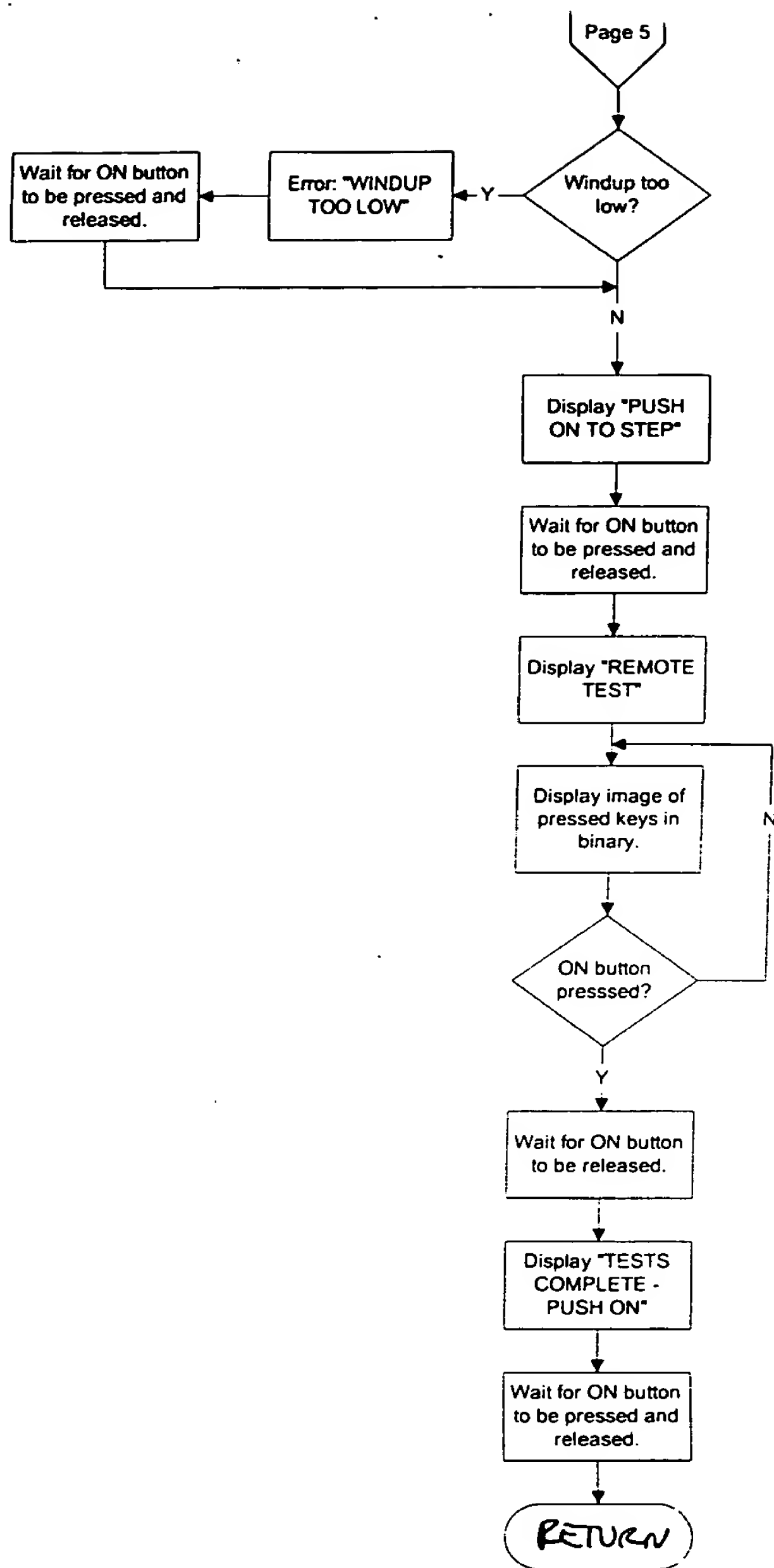


FIG. 19c

090306701.001001

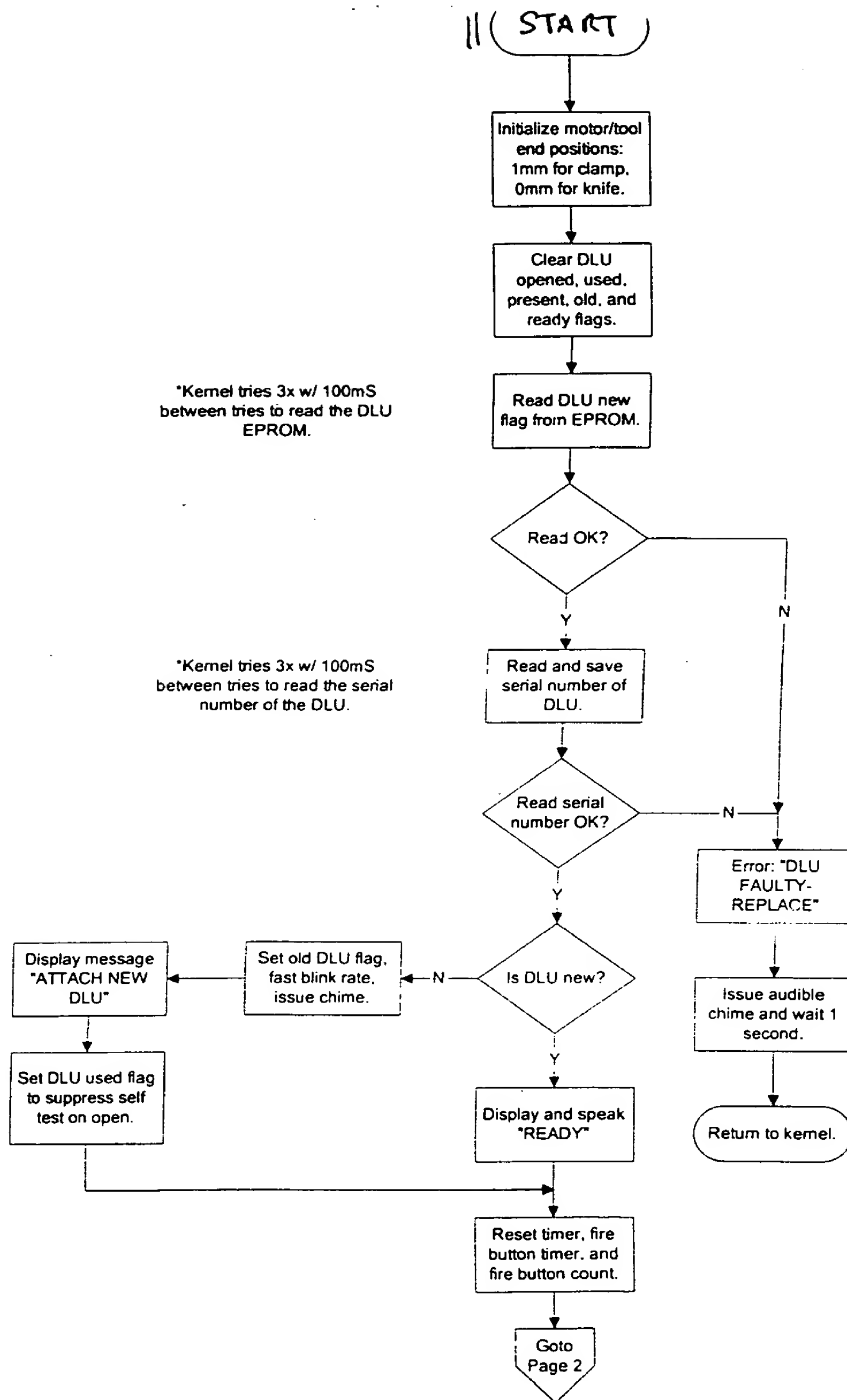


FIG. 20a

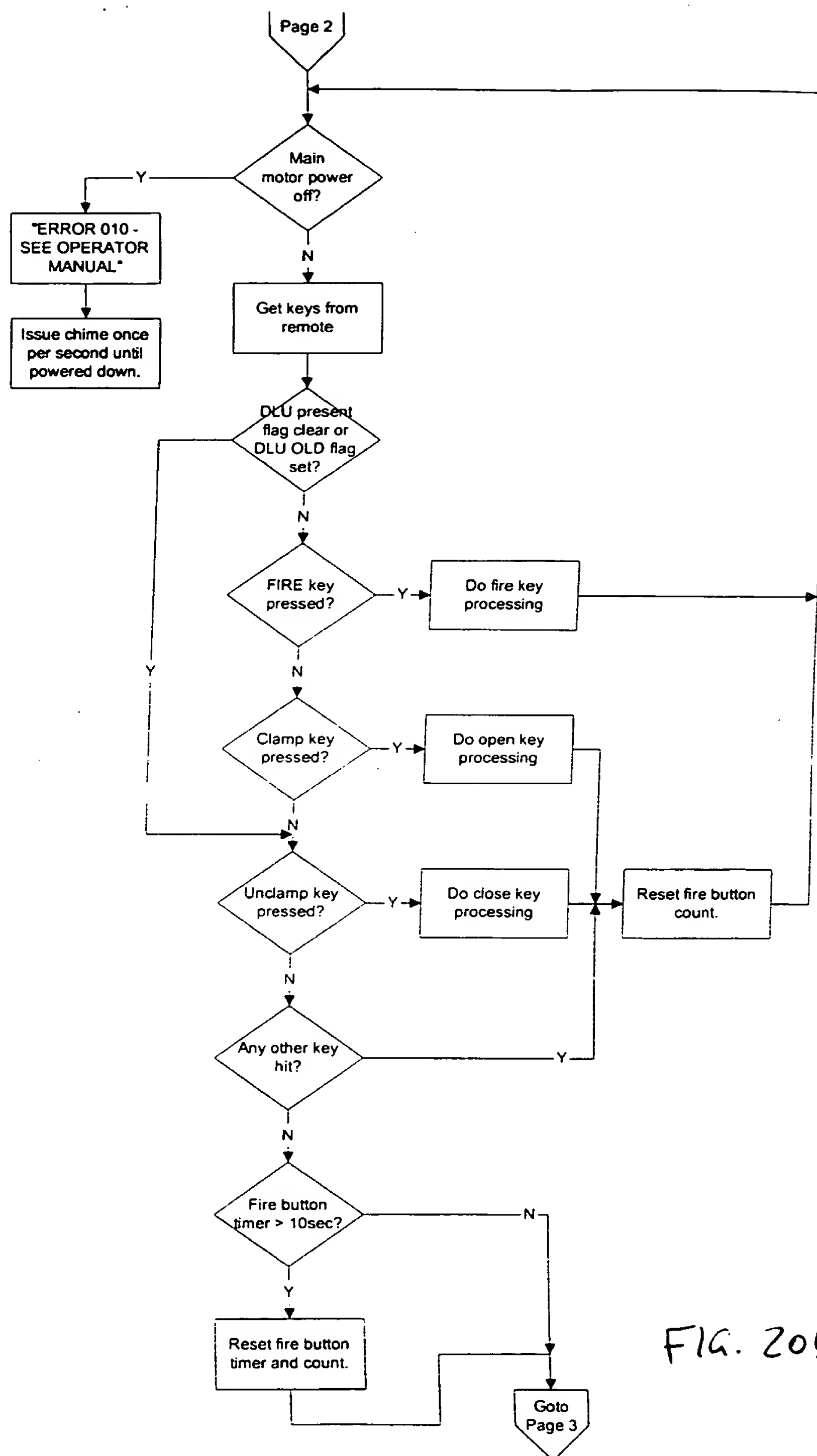
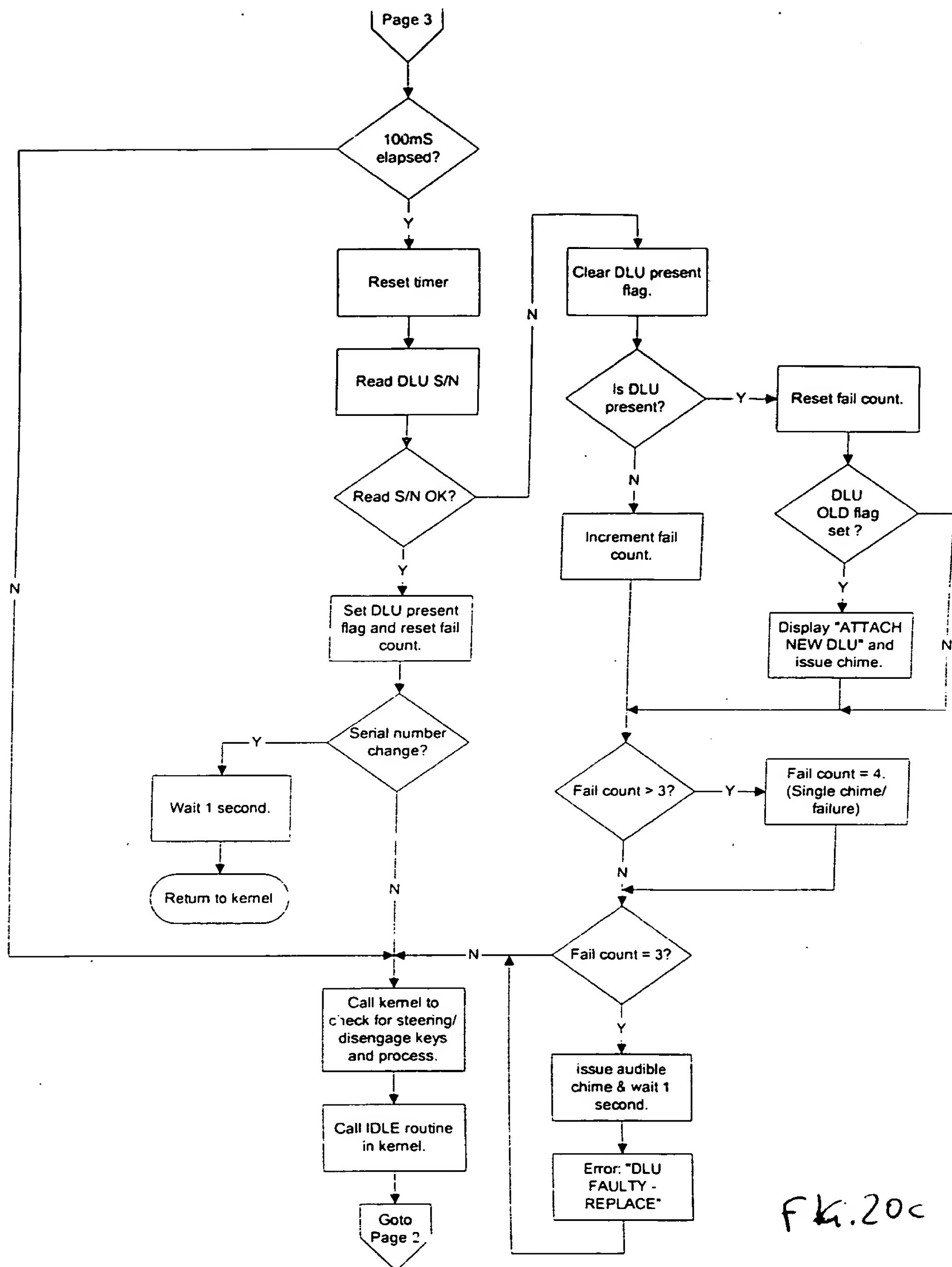
[illegible]

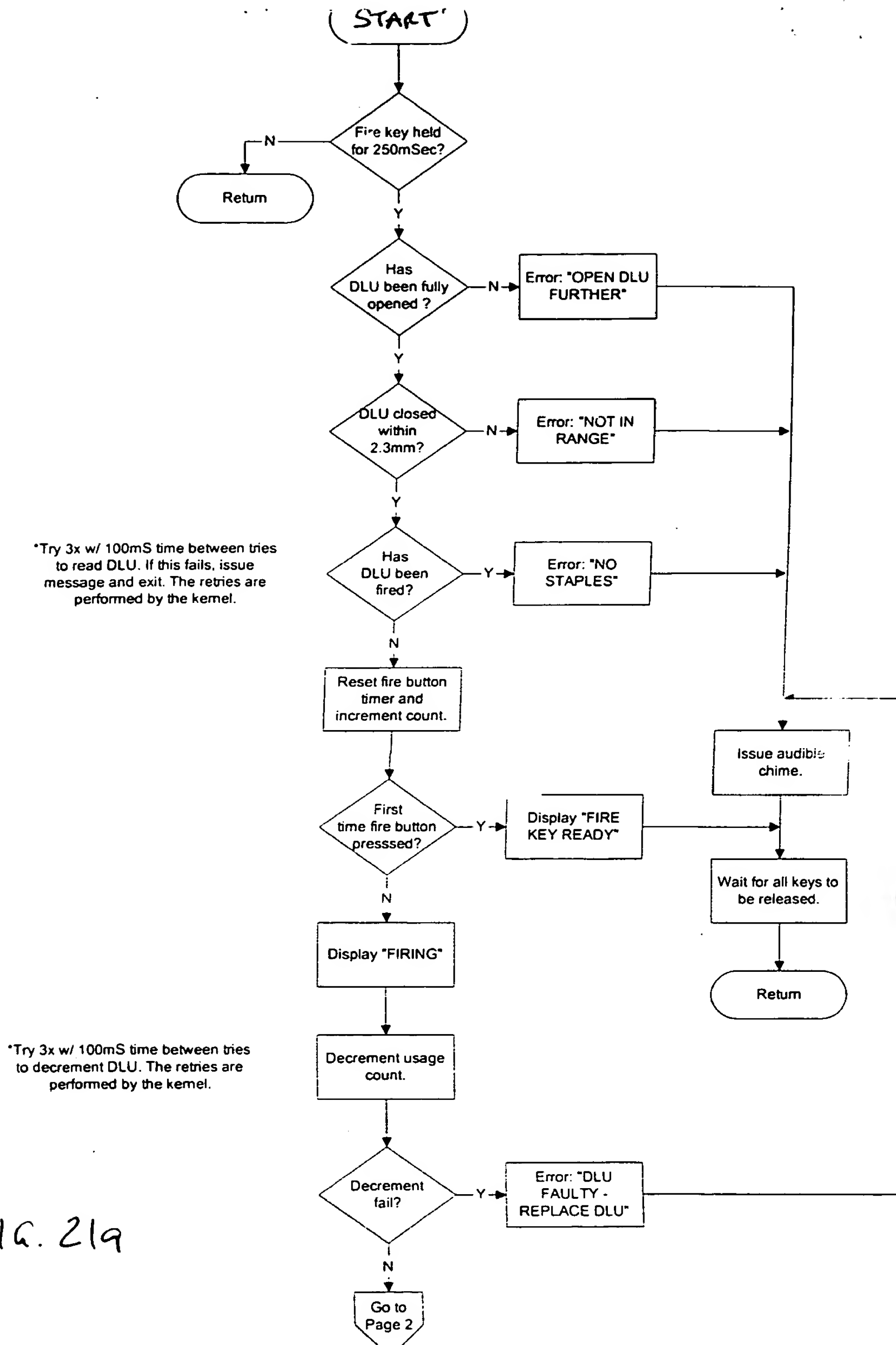
FIG. 20b

09030701 061001

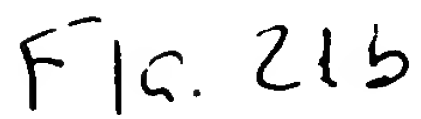


09036701.061301

FIG. 219



Windup threshold is
defined as
SHAFT_WINDUP_MAX



Page 3

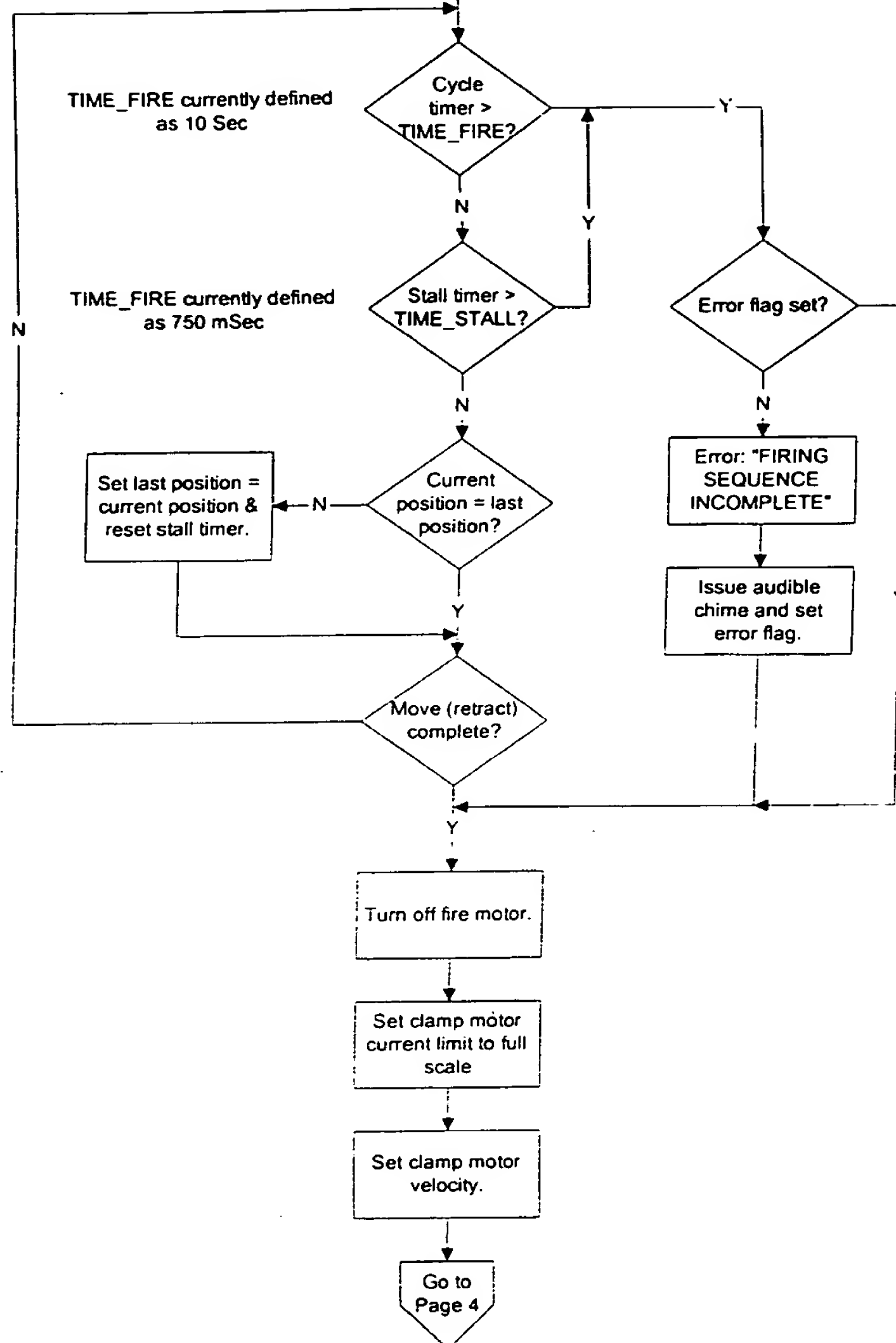
[illegible]

FIG. 21c

05036781-061804

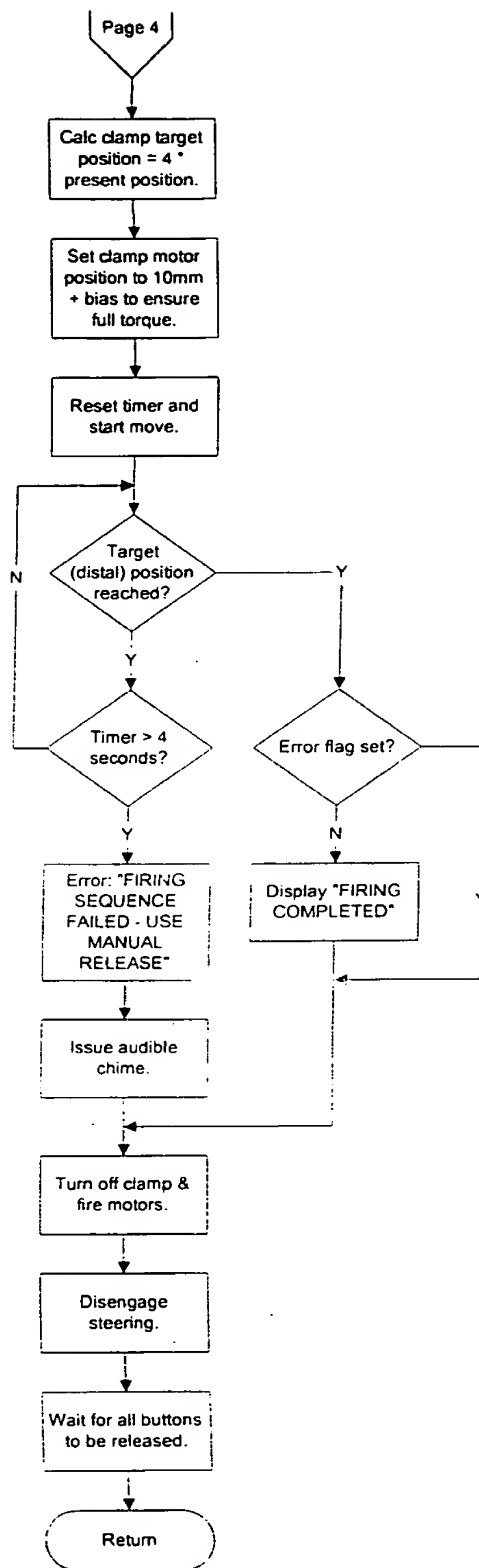
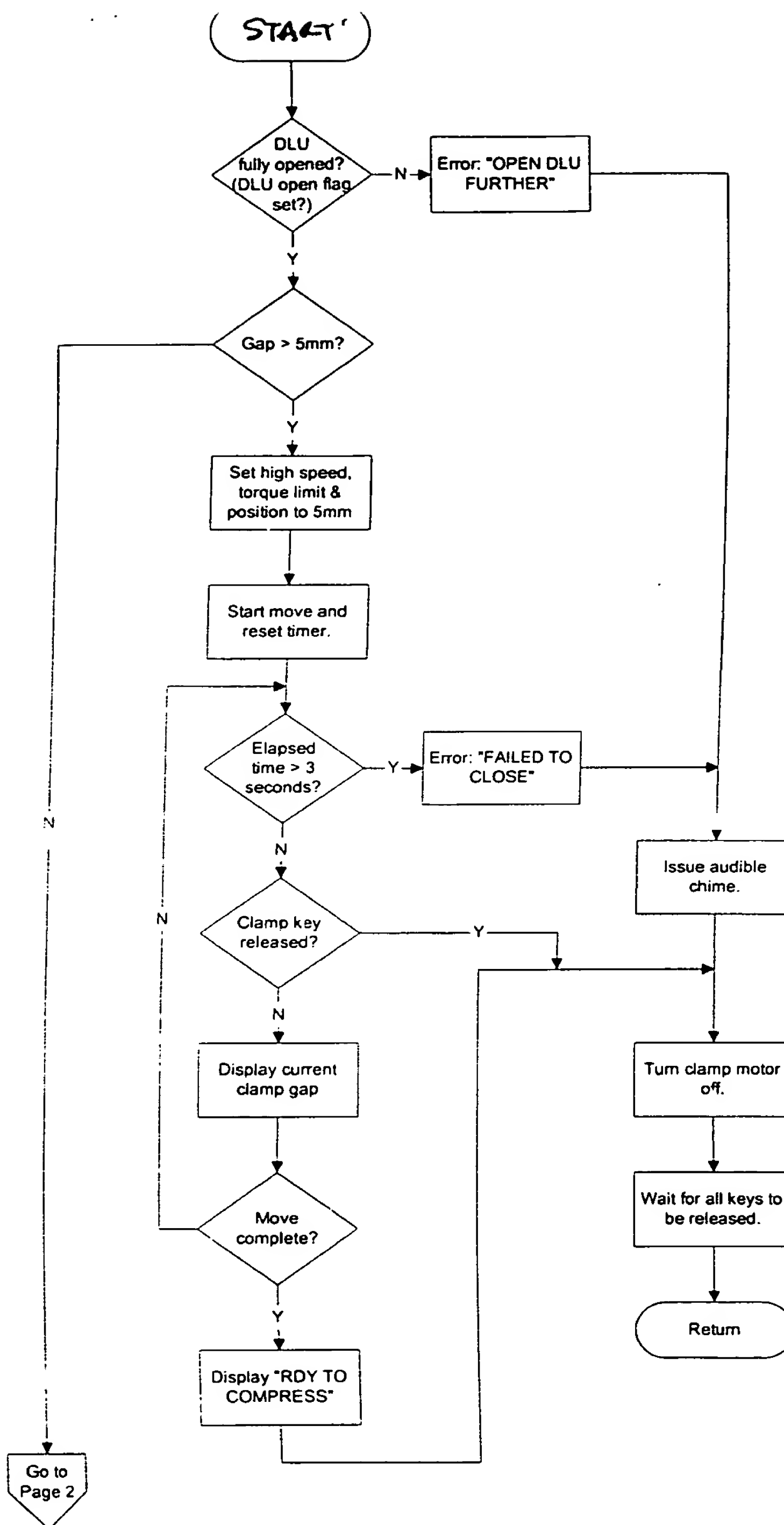


FIG. 21d

093367B1 061001

FIG. 22a



09036781.061801

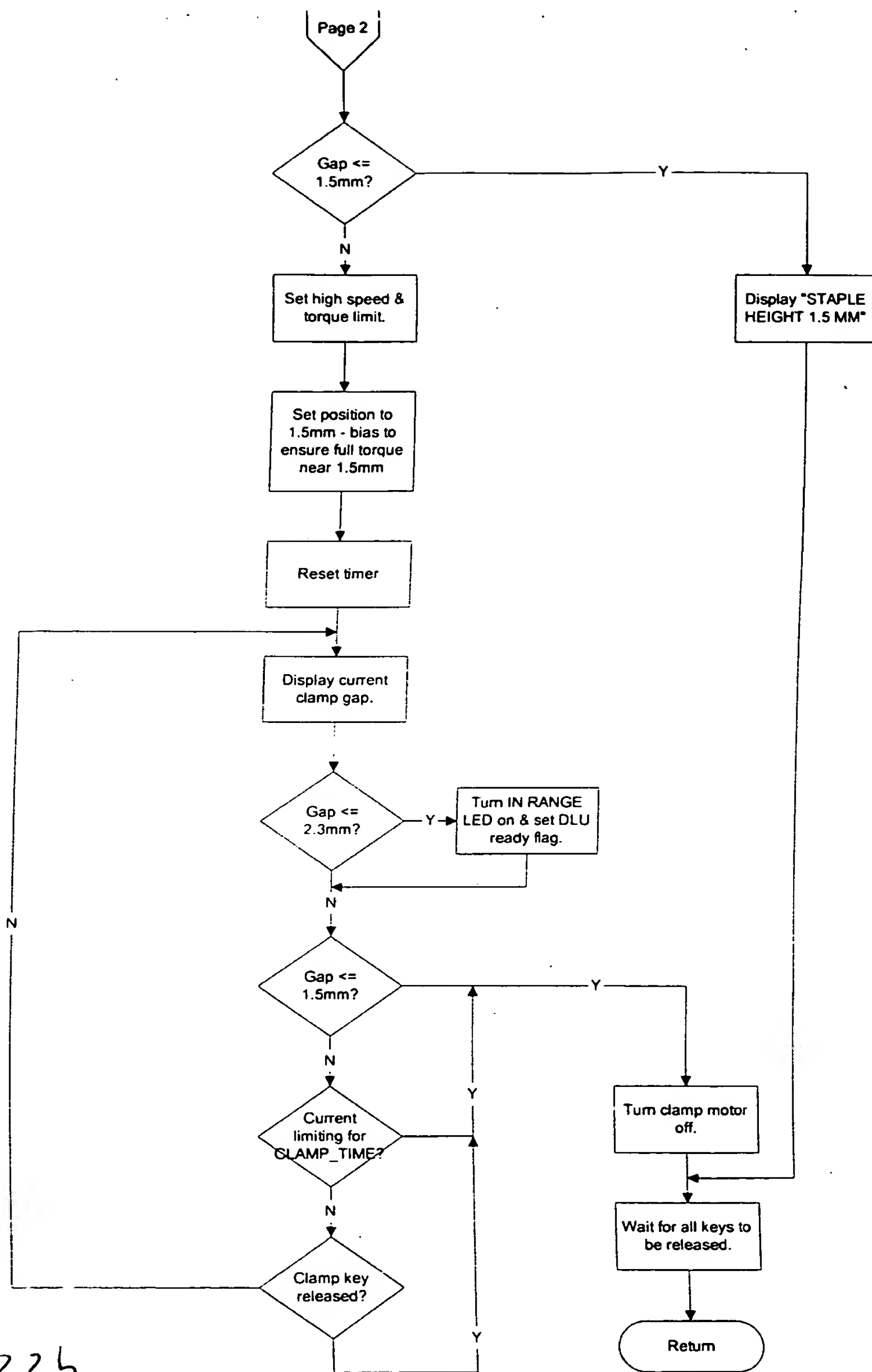
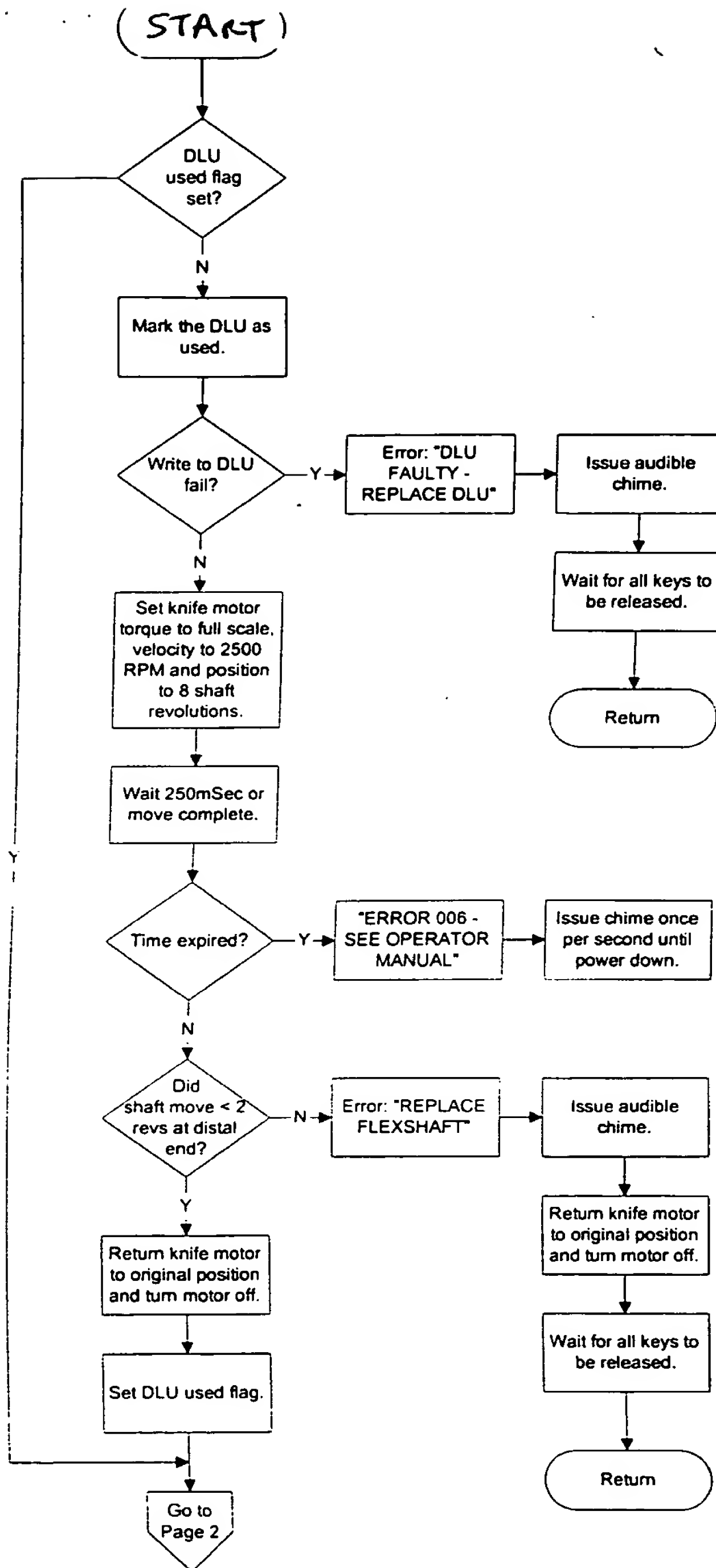


FIG. 22b

09636781.063601

FIG. 23a

*The kernel tries 3x w/ 100mS time between tries to write DLU.



09036781 061304

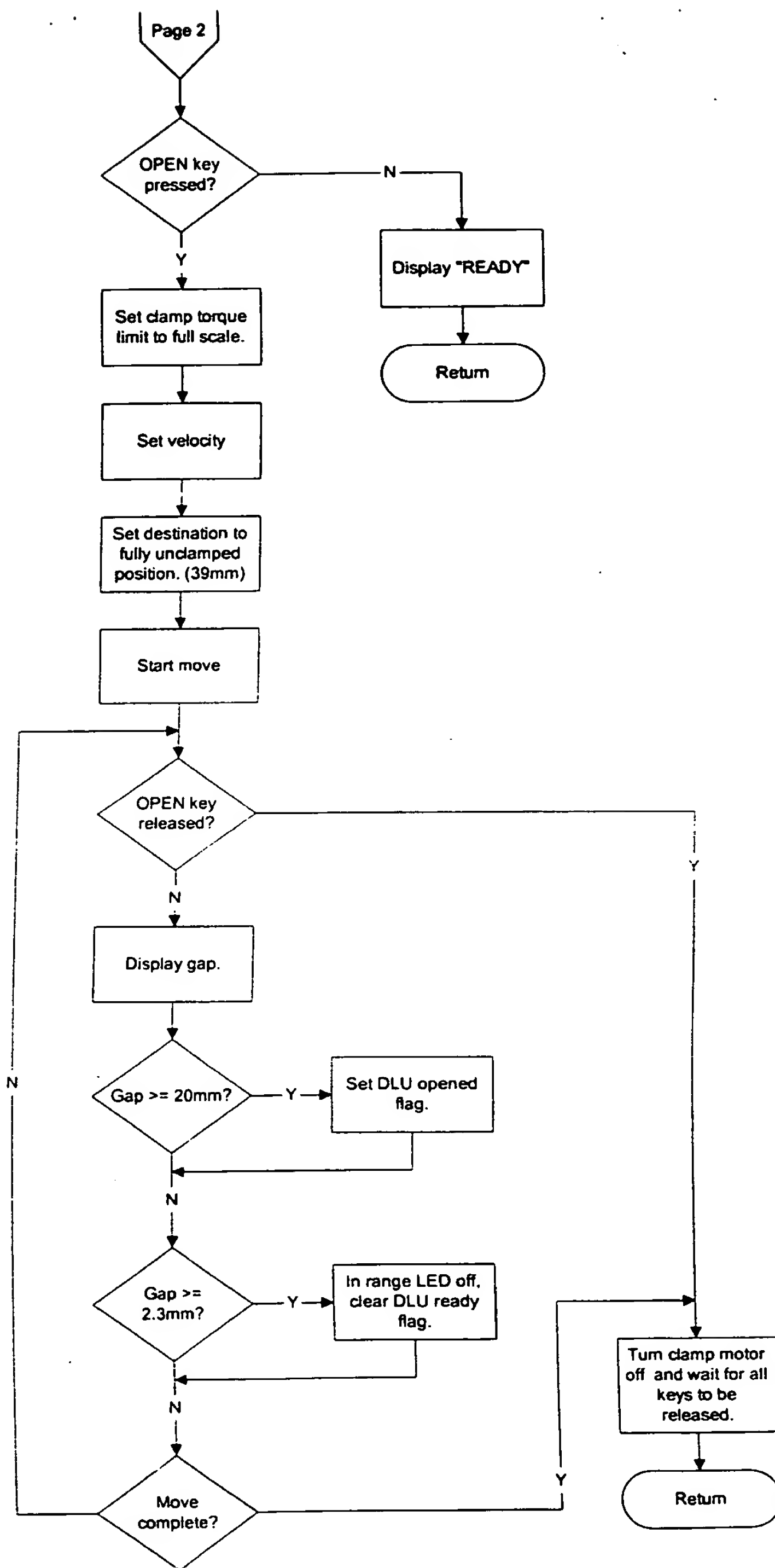


FIG. 23b